



Article Enhanced Properties of Extended Wavelength InGaAs on Compositionally Undulating Step-Graded InAsP Buffers Grown by Molecular Beam Epitaxy

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Copyright: © 2021 by the authors. Licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC BY) license (https:// creativecommons.org/licenses/by/ 4.0/). Abstract: The extended wavelength InGaAs material ($2.3 \mu m$) was prepared by introducing compositionally undulating step-graded InAs_yP_{1-y} buffers with unequal layer thickness grown by solid-source molecular beam epitaxy (MBE). The properties of the extended wavelength InGaAs layer were investigated. The surface showed ordered crosshatch morphology and a low roughness of 1.38 nm. Full relaxation, steep interface and less than one threading dislocation in the InGaAs layer were demonstrated by taking advantage of the strain compensation mechanism. Room temperature photoluminescence (PL) exhibited remarkable intensity attributed to the lower density of deep non-radiative centers. The emission peak energy with varied temperatures was in good agreement with Varshni's empirical equation, implying high crystal quality without inhomogeneity-induced localized states. Therefore, our work shows that compositionally undulating step-graded InAsP buffers with a thinner bottom modulation layer, grown by molecular beam epitaxy, is an effective approach to prepare InGaAs materials with wavelengths longer than 2.0 μm and to break the lattice limitation on the materials with even larger mismatch.

Keywords: extended wavelength InGaAs; photoluminescence; strain compensation; InAsP buffer

1. Introduction

The ternary $In_xGa_{1-x}As$ alloy has gained great success on optoelectronic applications due to the tunable bandgap from $0.9 \sim 3 \mu m$, which is corresponding to the atmosphere transmission window [1,2]. For example, In_{0.53}Ga_{0.47}As, which is lattice-matched to InP materials, has been widely used in multi-junction photovoltaics and commercially available in opto-communications [3–6]. In recent years, extended wavelength $In_xGa_{1-x}As$ (x > 0.53) and derived quaternary alloy have attracted more and more attention in thermophotovoltaics (TPV), remote sensing, and thermal imaging [7-12]. The lattice mismatch to InP substrate can be up to 3.2% (x = 1), and misfit dislocation will be generated to relax strains in the active epilayer during growth, which will deteriorate the device performances [13]. At this point, appropriate buffers with moderate thickness, enough relaxation and low density of dislocation must be applied to migrate the lattice mismatch. Many efforts have been taken to improve the quality of the $In_xGa_{1-x}As$ epilayer. Some researchers used linear graded buffers to stop the propagation of dislocation and others took strained superlattice to engineer the strain field [14–18]. Compositionally step-graded buffers were also employed. $In_xAl_{1-x}As$ and $InAs_yP_{1-y}$ were commonly used materials. Gu et al. studied properties of $In_{0.85}Ga_{0.15}As$ on different $In_xAl_{1-x}As$ buffer structures systematically grown by gas-source

molecular beam epitaxy (GSMBE) at several advantages, and In_{0.85}Ga_{0.15}As photodetectors with cutoff at 2.5 μm were made based on device-quality epilayer growth [14,19]. Hudait et al. reported $In_{0.69}Ga_{0.31}As$ TPV device with step-graded $InAs_vP_{1-v}$ buffer, and compared the $In_{0.69}Ga_{0.31}As$ properties with $In_xAl_{1-x}As$ and $InAs_yP_{1-y}$ buffers grown by solid-source molecular beam epitaxy (SSMBE), showing that $InAs_vP_{1-v}$ displayed ideal characteristics, while $In_xAl_{1-x}As$ presented phase decomposition and rougher surface [20]. Our group have reported 0.6 eV $In_{0.69}Ga_{0.31}As$ TPV device (corresponding band-edge emission at 2.0 μ m) on compositionally undulating step-graded InAs_vP_{1-v} buffer with equal layer thickness grown by metal organic chemical vapor deposition (MOCVD), and a lower surface roughness of 2.74 nm was achieved than that on conventional step-graded buffers [8,9]. For further extend wavelength, thicker buffers would be taken and more indium must be incorporated. However, it is still challenging to realize InGaAs with wavelength of over 2.0 µm through MOCVD because of its high growth temperature. Owing to the low growth temperature combined with the accurate control of growth rate and the solid-source beam flux, as well as the convenience of As and P beam flux ramping via needle valves, SSMBE is technically preferred. In this paper, the structural property of the SSMBE grown $In_{0.76}Ga_{0.32}As$ epilayer on a compositionally undulating step-graded $InAs_vP_{1-v}$ buffer with unequal single layer thickness is investigated in detail by using atomic force microscopy (AFM), reciprocal space mapping (RSM), and cross-sectional transmission electron microscopy (TEM). The excitation power dependence of photoluminescence (PL) peak energy at 10 K and the temperature-dependent photoluminescence from 10 K–300 K are also examined and discussed.

2. Experiments

2.1. Growth of Extended Wavelength InGaAs Structure

All samples were performed by a Veeco Gen20A all solid-source MBE (SSMBE) system equipped with arsenic (As) and phosphorus (P) cracker cells. The source flux in beam equivalent pressure (BEP) was measured by beam flux meter (BFM). Prior to material growth, surface pretreatment of semi-insulated InP (100) substrate was taken in preparation module at 350 °C for 1 h, followed by thermal desorption in growth module at 495 °C (monitored by pyrometer) within P_2 ambient for 5 min. A 100 nm undoped InP buffer layer was grown at 490 $^{\circ}$ C to get a better surface at first, and then the undoped InAs_vP_{1-v} buffer layers were grown at a constant temperature of 475 °C with a growth rate of 1.5 ML/s under stabilized P₂ BEP with varied As₂ BEP. A compositionally undulating stepgraded (CUSG) buffer structure with unequal layer thickness was applied in $InAs_vP_{1-v}$ buffer. Each step consisted of a 30 nm bottom modulation layer with high As composition and a 180 nm upper modulation layer with relatively lower As composition. Then, an "overshoot" InAs_{0.52}P_{0.48} layer, which has lattice misfit extent of 1.7% with respect to InP, was intentionally grown to further compensate the residual strain, followed by InAs_{0.49}P_{0.51} "virtual substrate" growth. The total thickness of $InAs_vP_{1-v}$ buffers was about 2.09 µm. After that, the extended wavelength In_{0.76}Ga_{0.24}As layer growth at 495 °C with a growth rate of 1.2 ML/s was grown (noted as sample A after). For comparison, $In_{0.76}Ga_{0.24}As$ on common compositionally step-graded (CSG) $InAs_vP_{1-v}$ buffer (noted as sample B after) with a total thickness of 2.96 µm was also grown. Detailed structure was schematically illustrated in Figure 1.

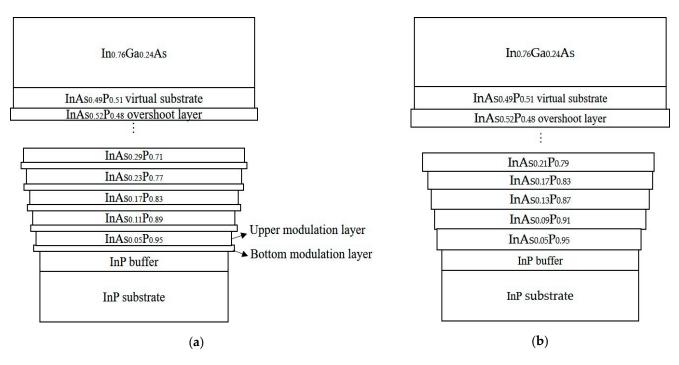


Figure 1. (a) Schematic diagram of extended wavelength InGaAs on InP substrate with compositionally undulating step-graded (CUSG) buffer structure (noted as sample A). (b) Schematic diagram of $In_{0.76}Ga_{0.24}As$ on InP substrate with common compositionally step-graded buffer structure (noted as sample B).

2.2. Measurements of Structural and Optical Properties

After the completion of epitaxy, surface morphology was characterized by using Bruker Dimension ICON atomic force microscopy (AFM) in tapping mode. The asymmetric strain relaxation properties were checked by reciprocal space mapping (RSMs) using highresolution triple axis XRD via Bruker D8 Discover, then microstructures and defects were evaluated by FEI Talos20 transmission electron microscope (TEM) operating at 200 kV on cross-sectioned samples, which were prepared by focus ion beam (FIB) microscope (FEI Scios), and PL measurements were conducted on a sample that was mounted on the cold finger of a Janis closed cycle cryostat with a varying temperature range of 10 K–300 K. The excitation was 532 nm laser with chopper, and the emission signal from the sample was dispersed with a Horiba iHR350 monochromator, amplified with lock-in amplifier (SHR380) and detected by InAs detectors.

3. Results and Discussion

3.1. Surface Morphology

The 20 μ m × 20 μ m AFM micrographs and related line profiles in [110] direction of two samples are shown in Figure 2. Characteristic undulating morphology with hills and valleys parallel to the intersection of slip planes with the crystal surface, namely, "crosshatch" pattern can be obviously observed. This surface characteristic is typical surface morphology and appeared after plastic strain relaxation in the heteroepitaxy of mismatched zinc-blende structure layers, which means InGaAs is grown in two-dimensional mode with ideal strain relaxation [21]. The root mean square roughness is 1.38 nm and 3.60 nm for sample A and sample B, respectively. Furthermore, the peak-to-valley height of sample B is larger than sample A, as shown in Figure 2c. The results indicate that CUSG structure is better-developed and can smoothen the surface more efficiently, similar conclusion was highlighted [8]. It should be stressed that the thickness of the bottom modulation layer in our case is much thinner. Thinner compositional overshoot could remarkably improve structural and optical properties [22]. In this work, every thin bottom modulation InAs_vP_{1-v} layer is compositional overshoot, and lattice is under compressive strain, which should result in hills. However, the thickness does not exceed the critical thickness with respect to the former upper modulation layer, so it provides a relatively flat surface for the following upper modulation layer growth. Under the action of alternatively compressive and tensile strain in CUSG structure, superior surface morphology could be achieved. Therefore, one can conclude that the CUSG structure will provide a better surface morphology relating to higher crystalline quality of the In_{0.76}Ga_{0.24}As layer, and thus lead to a much lower dislocation density and better optical property in sample A.

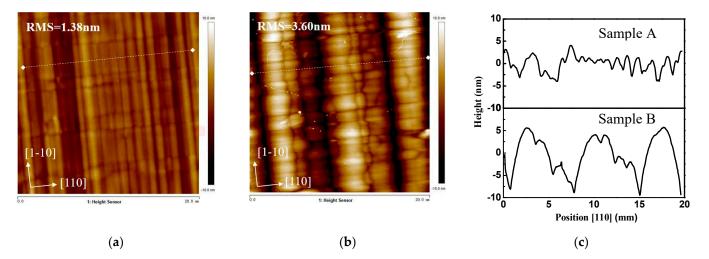


Figure 2. AFM images of extended wavelength InGaAs on InP substrate at a scanning area of 20 μ m × 20 μ m: (**a**) sample A (RMS: 1.38 nm); (**b**) sample B (RMS: 3.60 nm); (**c**) the line profile along [110] direction of sample A and sample B.

3.2. The Structural Property

The relaxation state of entire III-V epilayers could be characterized by RSMs. According to the research by Hornstra et al. The equivalent bulk lattice mismatch f can be expressed as [23,24]:

$$f = (f_{\perp} - f_{//})\frac{1 - v}{1 + v} + f_{//}$$
(1)

where $f_{\perp} = \frac{\Delta a_{\perp}}{a_s}$, and $f_{//} = \frac{\Delta a_{//}}{a_s}$ are lattice mismatch in the perpendicular and parallel directions, respectively. The *v* is the Poisson's ratio, for In_{0.76}Ga_{0.24}As, v = 0.34 by using elastic constants of In_xGa_{1-x}As material at 300 K. The strain relaxation of In_xGa_{1-x}As grown on the InP substrate can be described by the following equation [24]:

$$R = \frac{a_{//} - a_s}{a_{relaxed} - a_s} = \frac{f_{//}}{f}$$
(2)

where $a_{//}$, and $a_{relaxed}$ represent the in-plane lattice parameters of the epilayer and lattice parameters under completely relaxed state, and a_s is the lattice parameters of the substrate.

The RSMs around asymmetric (115) reflection of $In_{0.76}Ga_{0.24}As/InP$ samples were carried out (as shown in Figure 3). Three distinct reciprocal lattice points (RLP) maxima can be found, which corresponded to (i) the InP substrate; (ii) the uppermost InAsP layer of the InAs_yP_{1-y} buffers; and (iii) the extended wavelength $In_{0.76}Ga_{0.24}As$ epilayer, respectively. The vertical solid line represents a fully strained and inclined solid line, which is extended to $Q_x = 0$ and represents a fully relaxed epilayer. More information about the lattice parameter could be obtained from the position and the intensity distribution of RSMs. By using Equations (1) and (2), the percentage of strain relaxation is 96.47% and 85.12% for sample A and sample B, respectively, implying that the $In_{0.76}Ga_{0.24}As$ layer and the uppermost InAsP layer almost relaxed completely for sample A. Whereas, residual strain still existed in the uppermost InAsP layer and the $In_{0.76}Ga_{0.24}As$ layer of sample B; therefore, some of threading dislocations may penetrate into the InGaAs layer.

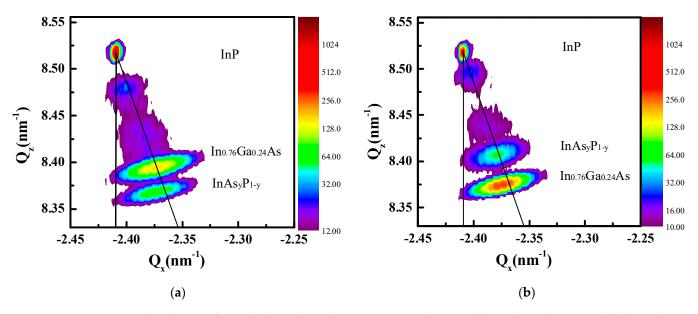
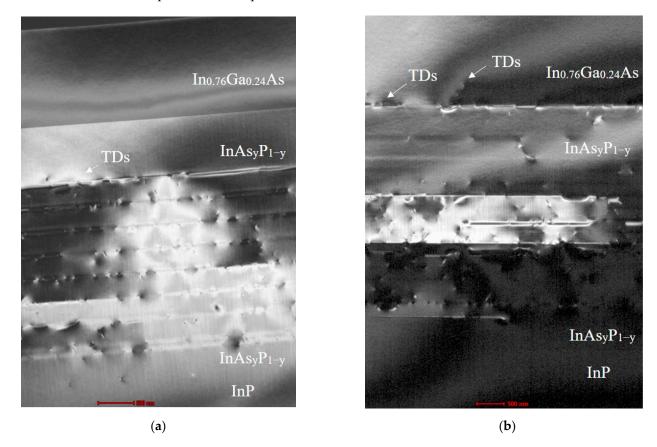


Figure 3. Asymmetric (115) RSMs of extended wavelength InGaAs on InP substrate: (**a**) sample A (R = 96.47%); (**b**) sample B (R = 85.12%).

Further investigation into the structural property of the two samples is provided by cross-sectional TEM analysis, as shown in Figure 4, and the $In_{0.76}Ga_{0.24}As$ layer and $InAs_vP_{1-v}$ buffer layers are labeled in these figures. Figure 4a shows the sample grown on the CUSG buffers. Misfit dislocations are restricted along with the internal interface, dislocations are significantly decreased in the uppermost InAsP layer, and no threading dislocation is observed in the $In_{0.76}Ga_{0.24}As$ layer of sample A. This mainly benefits from the strain compensation around the interface by the insertion of tensile $InAs_vP_{1-v}$ layers, which can change the dislocation glide direction and facilitate the dislocation annihilation, and the interfaces also prevent the threading dislocations from propagating vertically through the structure [25]. It is a reason of the superior surface and better two-dimensional crosshatch pattern in sample A, as illustrated in AFM analysis. Generally speaking, the TDD decreases with a thicker buffer layer [26], so less defects in sample B should appear regarding a $\sim 0.9 \,\mu m$ thicker buffer than that of sample A. However, as shown in Figure 4b, although most of the dislocations are confined within the $InAs_vP_{1-v}$ buffers for sample B, there are still some dislocations penetrating the bottom of the InGaAs layer. As no other treatment is applied, it is reasonable to conclude that the residual strain contributes to the penetration of dislocations into the InGaAs layer, which is consistent with the RSMs' relaxation analysis discussed above. Moreover, it is also clear that the poor surface morphology of sample B is due to the dislocation penetration result of incomplete relaxation [27].

3.3. The Optical Property

The PL spectra of InGaAs epilayer with varying excitation power measured at 10 K are shown in Figure 5. Single band edge peak is observed in sample A, while an additional low-energy shoulder peak appears in sample B. Such asymmetry may be attributed to localized trap states related to structural defects [12]. For the sake of comparison, PL spectra of sample B are deconvoluted using multiple Gaussian curves, and Figure 5b presents excitation power dependence of band edge peak and shoulder peak. It can be seen from that, the band edge peak keeps almost constant with varying excitation power, although an insignificant red-shift with increasing excitation power is shown in sample A. This may be caused by increasing temperature at the measured point, so sample A is probably sensitive to temperature variation. On the other side, the low-energy shoulder in sample B increases from 0.478 eV to 0.486 eV when the excitation power increases from 33 mW to 258 mW, and the slight shift of the shoulder peak is also exhibited in In-rich In_xGa_{1-x}As samples, which is mainly due to defect-related energy band filling effect [12]. In addition, the intensity



tends to saturate at high excitation power, which is normally a signature of the localization phenomena for potential fluctuations or carrier transfer in the band-tail [28].

Figure 4. Cross-sectional TEM images of extended wavelength InGaAs on InP substrate: (a) sample A and (b) sample B.

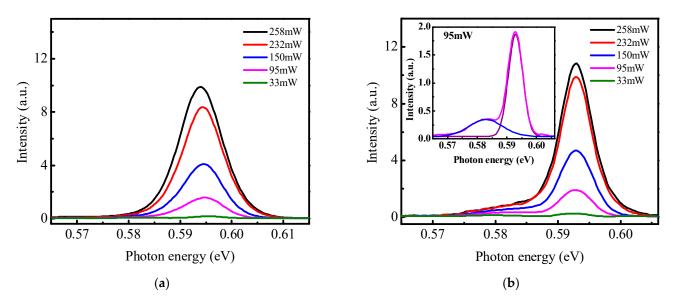


Figure 5. (a) PL spectra of extended wavelength InGaAs on InP substrate measured at 10 K (smoothed): (a) sample A and (b) sample B (the inset is Gaussian decomposition of PL spectrum at 95 mW).

Figure 6a presents PL spectra measured at room temperature for sample A and sample B, and the peak wavelength is 2297 nm and 2302 nm, respectively. An asymmetric line shape with a long high-energy tail and relatively faster declining low-energy edge is displayed for both samples, especially for sample A. This kind of profile is usually explained

by a conventional band-to-band transition model, which predominately reflects the density of states of three-dimensional electronic structure and the Boltzmann distribution of carriers or free excitons [29]. Moreover, it is worth mentioning that the intensity of sample A is about 5 times stronger than that of sample B.

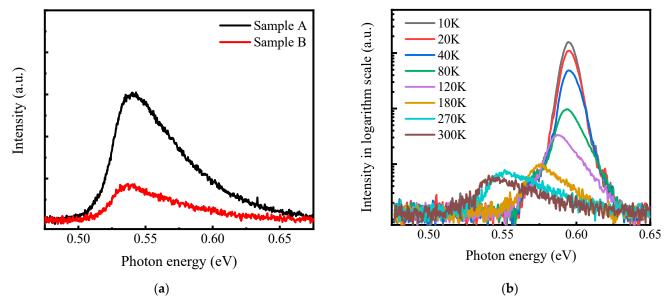


Figure 6. (a) PL spectra of extended wavelength InGaAs sample at room temperature. (b) PL spectra of extended wavelength InGaAs sample A at different temperatures from 10 K to 300 K (Intensity is plotted in logarithm scale).

Temperature-dependent photoluminescence (TDPL) measurements is also performed from 10 K to 300 K, as shown in Figure 6b. Peak energy shows red shifted when the temperature is higher than 40 K, which is due to band shrinkage effect (thermal activation of carriers). The peak position and the intensity are extracted from those spectra at each temperature.

The relationship between peak energy and temperature can be described by Varshni's empirical equation as following [30]:

$$E(T) = E(0) - \frac{\alpha T^2}{T + \beta}$$
(3)

where E(0) is the energy gap at 0 K, α plays the role of $T \rightarrow \infty$ limiting value of band-gap shrinkage coefficient, and β is an associated parameter related to the Debye temperature. As mentioned in excitation power-dependent PL, localized states caused by potential fluctuations or carrier transfer in the band-tail may exist. The inhomogeneity of indium composition can result in fluctuation of energy band, thus, an atypical S-shaped behavior of peak energy with temperature will be presented [12]. Unlike the localized states induced Sshaped behavior highlighted in Ref. [12], this work produces good fits to the experimental data for the entire temperature range. The behavior indicates that the distribution of indium composition is homogeneous in the MBE-grown extended wavelength InGaAs material. The fitted equation is also presented in Figure 7, and the Varshni parameter α determined in sample A is higher than that of sample B. A higher α means that sample A is relatively more sensitive with temperature, which is in accordance with the insignificant red-shift with increasing excitation power of sample A [13].

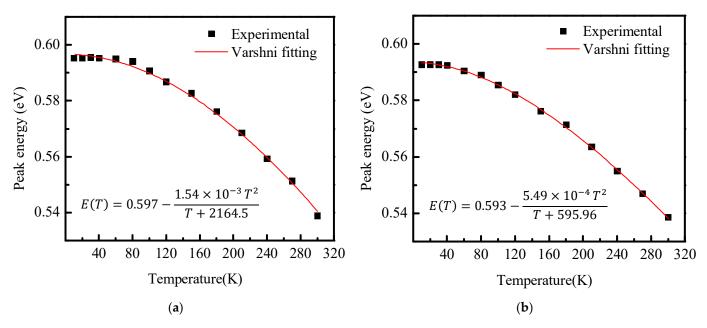


Figure 7. The peak energy of extended wavelength InGaAs on InP substrate as a function of temperature (**a**) sample A and (**b**) sample B, the fitting parameters are also given.

As can also be seen from Figure 6b, the overall intensity of the PL spectra diminishes significantly as temperature increases, which is due to the thermal quenching effect caused by phonon-assisted non-radiative recombination. In order to further understand the mechanism of carrier thermal quenching in the InGaAs layer, Arrhenius-like expression given in Equation (4) is applied to fit the experimental data of the PL intensity [31,32], in which two non-radiative recombination processes are assumed.

$$I(T) = \frac{I_0}{1 + C_1 exp\left(\frac{-E_{a1}}{k_B T}\right) + C_2 exp\left(\frac{-E_{a2}}{k_B T}\right)}$$
(4)

where *T* is the measured temperature, I(T) is the integrated PL intensity, and k_B is Boltzmann's constant. E_{a1} and E_{a2} are the thermal activation energy of non-radiative recombination processes. C_1 and C_2 are the ratio of non-radiative to radiative probability for the non-radiative mechanism, in other words, they are related to the density of non-radiative recombination centers.

Figure 8 shows the relationship between intensity and the best fit using Equation (4) with the experimental data, and the activation energy obtained from the fit for each sample are also indicated. The value of E_{a1} , which is mainly due to interfacial roughness, is 5.44 meV and 6.24 meV of sample A and sample B, respectively. It indicates that the localized energy caused by interfacial roughness is small. The second process, which is predominant at higher temperatures, mainly results from the suppression of non-radiative recombination centers linked to misfit dislocations, and it is said that higher E_{a2} values may be associated with improved internal quantum efficiency [13]. In our case, E_{a2} values are 26.67 meV and 38.03 meV for sample A and sample B, respectively. Sample A seems to perform worse at room temperature. In fact, it is effectively the transition probability C_2 of the recombination that governs the PL intensity at room temperature [31]. We could see that C_2 of sample A is about 5 times smaller than that of sample B, indicating much less misfit dislocation in sample A, in line with the results evidenced in TEM. Hence, stronger PL intensity at room temperature is displayed.

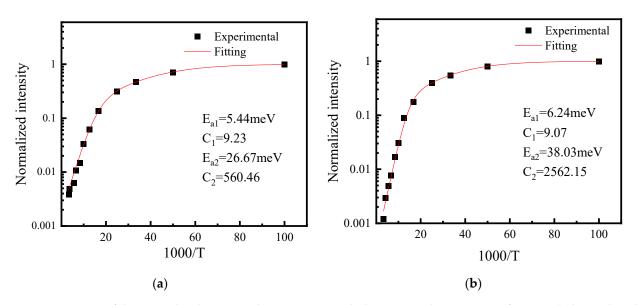


Figure 8. Variation of the normalized integrated PL intensity with the reciprocal temperature for extended wavelength InGaAs on InP substrate. The red solid lines represent the Arrhenius fitting results: (**a**) sample A; (**b**) sample B.

4. Conclusions

Extended wavelength (2.3 µm) InGaAs on common and compositionally undulating step-graded InAsP buffers have been grown by SSMBE, and their structural and optical properties have been investigated by means of AFM, RSM, TEM, and PL measurements. By taking advantage of the strain compensation mechanism, a smaller surface roughness of 1.38 nm, a better relaxation of 96.47%, and fewer dislocations were presented for the sample with improved CUSG buffer structure and thinner buffers. Furthermore, the roomtemperature PL intensity of InGaAs grown on a CUSG buffer showed ~5 times stronger than the one grown on a CSG buffer, which could be attributed to less deep non-radiative centers in the In_{0.76}Ga_{0.24}As layer. The emission peak energy with varying temperatures was in good agreement with Varshni's empirical equation, which implies high crystal quality without inhomogeneity-induced localized states under a low growth temperature. In addition, the InGaAs grown on a CUSG buffer is slightly more sensitive to temperature. To sum up, our work shows that compositionally undulating step-graded InAsP buffers with a thinner bottom modulation layer, grown by SSMBE, is an effective approach to prepare InGaAs materials with wavelengths longer than 2.0 µm and to break the lattice restriction on the materials with even larger mismatch.

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Data Availability Statement: The data presented in this study are available within the manuscript.

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Conflicts of Interest: The authors declare no conflict of interest.

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