

Article

Enhancing the Light-Extraction Efficiency of AlGaN-Based Deep-Ultraviolet Light-Emitting Diodes by Optimizing the Diameter and Tilt of the Aluminum Sidewall

Yung-Min Pai ¹, Chih-Hao Lin ¹, Chun-Fu Lee ¹, Chun-Peng Lin ¹, Cheng-Huan Chen ¹, Hao-Chung Kuo ^{1,*} and Zhi-Ting Ye ^{2,*}

¹ Department of Photonics and Institute of Electro-Optical Engineering, National Chiao Tung University, Hsinchu 30010, Taiwan; f926202@gmail.com (Y.-M.P.); actupon@gmail.com (C.-H.L.); ian.727@gmail.com (C.-F.L.); alulgp@gmail.com (C.-P.L.); chhuchen@nctu.edu.tw (C.-H.C.)

² Department of Electro-Optical Engineering, National United University, 2, Lienda, Miaoli 26063, Taiwan

* Correspondence: hckuo@faculty.nctu.edu.tw (H.-C.K.); ZTYe@nuu.edu.tw (Z.-T.Y.)

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Abstract: To realize high-efficiency AlGaN-based deep-ultraviolet light-emitting diodes (DUV-LEDs), enhancing their light-extraction efficiency (LEE) is crucial. This paper proposes an aluminum-based sidewall reflector structure that could replace the conventional ceramic-based packaging method. We design optimization simulations and experimental results demonstrated the light power output could be enhanced 18.38% of DUV-LEDs packaged with the aluminum-based sidewall.

Keywords: light emitting diode; deep-ultraviolet light-emitting diode; reflector inclined plane; metal diameter of cavity bottom

1. Introduction

Deep-ultraviolet light-emitting diodes (DUV-LEDs) with aluminum gallium nitride (AlGaN) components are gaining substantial attention given their potential for use in such applications as secure communication, chemical decomposition, disinfection, water and air purification, and biological detection systems [1–4]. DUV-LEDs are more advantageous than are mercury UV lamps given their more favorable characteristics, such as long life, robustness, low energy consumption, high environmental friendliness, and being free of mercury [5,6]. However, compared with white LEDs, DUV-LEDs have a lower emission wavelength of 250–300 nm and have lower luminous efficiency and long-term reliability [7–15]. Currently, white LED devices are typically packaged by combining LED chips with a transparent organic resin [16–18]. But DUV-LEDs use the same way as white light LED resin packages. Such resins degrade over time, resulting in considerable UV absorption. The consequent high contact resistance and low light-extraction efficiency (LEE) degrades the long-term reliability, high forward voltage, and very low external quantum efficiency (EQE). To ameliorate these drawbacks, silicon-glass overcaps and hermetic packaging have been used extensively [19–29]. However, the reflectivity of ceramic package decreased rapidly in Deep-UV region [30–34]. In this study, the characteristics of the DUV-LEDs, namely the diameter of the bottom cavity and the tilt angle of the reflector and to simulation comparison to aluminum, gold, and silver metal materials with better reflectivity, were optimized to improve the LEE of proposed DUV-LED structure.

Light Extraction Efficiency

In principle, an LED's external quantum efficiency is calculated using the following equation [35]:

$$\text{EQE} = \text{IQE} \times \text{LEE}$$

$$\text{LEE} = \frac{\text{No. of photons emitted (per second) into free space}}{\text{No. of photons emitted (per second) from the active surface}} \quad (1)$$

$$\text{EQE} = \frac{\text{No. of photons emitted (per second) into free space}}{\text{No. of electrons injected (per second) into the LED per second}}$$

where EQE and IQE are the external and internal quantum efficiency, respectively, and LEE is the light extraction efficiency from the LED chip to air.

2. Optical Design of DUV-LED Reflector Inclined Plane

Figure 1 illustrates the structures of the cavities of (a) a conventional DUV-LED (b) and a DUV-LED with the metal tilt reflector proposed in this study; both structures are composed of the same type of reflector plate and ultraviolet-transmitting glass. The conventional cavity is protected by ceramic, whereas the proposed cavity design uses metal and tilt reflection for cavity protection. In the proposed design, the LEE of the DUV-LED chip can be increased by optimizing the distance between the chip and the metal sidewall as well as the tilt reflection angle.

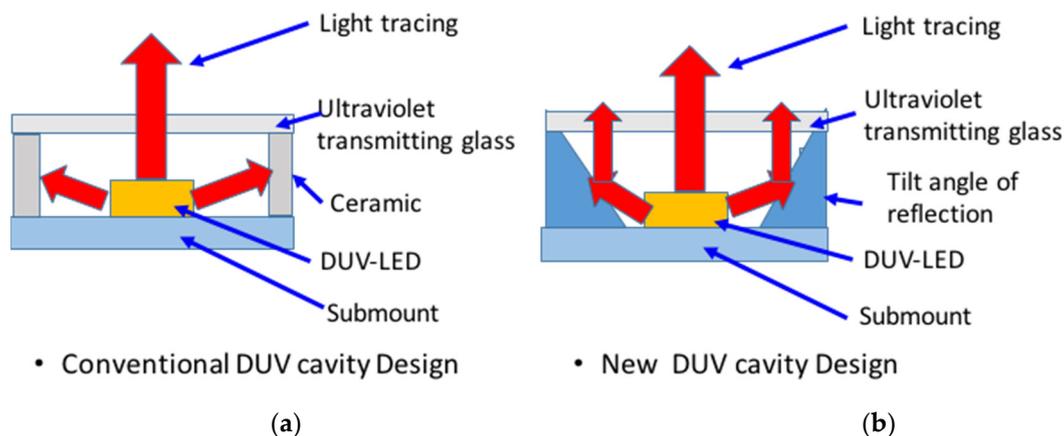


Figure 1. (a) Conventional deep-ultraviolet (DUV) cavity and (b) proposed metal tilt reflector design. LED: light-emitting diodes.

2.1. DUV-LED Specifications

In this study, we used DUV-LED chips (Lextar Electronics Corporation, Hsinchu, Taiwan) of approximately $0.45 \times 0.75 \text{ mm}^2$, whose emission spectra were captured at a 100-mA forward current. These chips exhibited a peak wavelength of 285 nm, with a full width at half-maximum of 10 nm (Figure 2). All data was measured using an integrating sphere (Isuzu Optical, Hsinchu, Taiwan). Figure 3 presents the chips' relative far-field emission pattern (arb. unit) as measured by a LEDGON goniometer (Instrument Systems GmbH, Munich, Germany). Table 1 lists the specifications (i.e., surface and material characteristics) of all components in the DUV-LED reflector cavity; these data were used in ray-tracing simulations to optimize the proposed DUV-LED design. To simplify numerical values, the subsequent simulations and experiment did not include the ultraviolet transmitting glass.

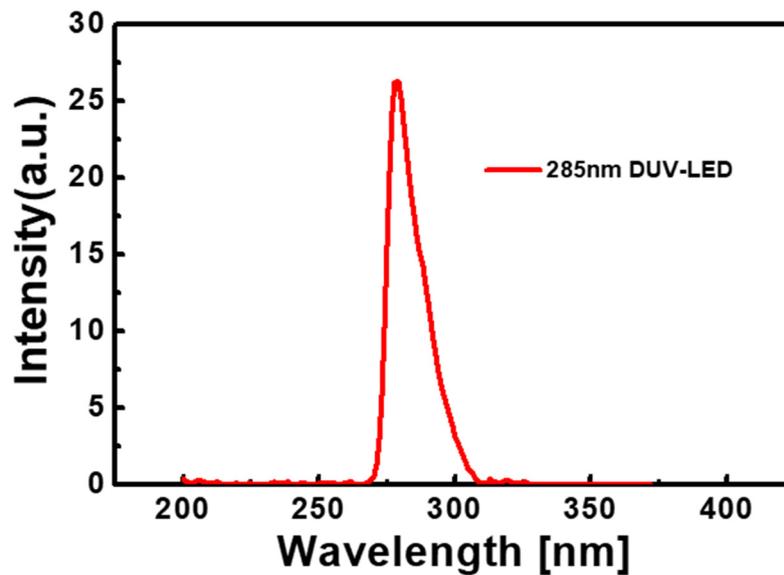


Figure 2. Normalized emission spectrum generated at 100 mA forward current of the proposed.

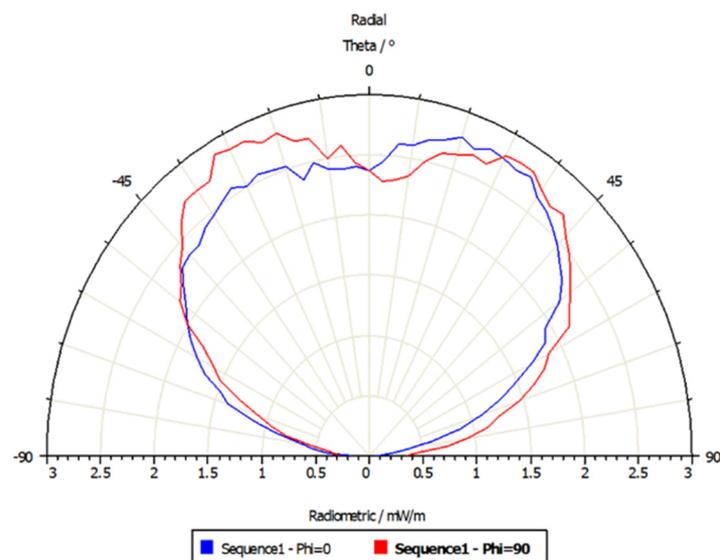


Figure 3. DUV-LEDs' relative far-field emission pattern (arb. unit).

Table 1. Surface and material characteristics of all components in the reflector cavity of the proposed deep-ultraviolet light-emitting diodes (DUV-LED).

Component	Characteristics	Material
DUV-LED	Peak wavelength = 285 nm	AlGaN
Cavity sidewall [36]	Mirror R = 92%	Aluminum
	Mirror R = 40%	Gold
	Mirror R = 25%	Silver
Packing structure	Polished	Ceramics

2.2. Ray-Tracing Simulation of DUV-LED Metal Diameter of Cavity Bottom

Ray-tracing simulation was performed using Light Tools™ (Synopsys Inc., Mountain View, CA, USA). Through simulation, we optimized the cavity design (Figure 4) of the proposed DUV-LED module by identifying the cavity diameter that yielded the optimal LEE.

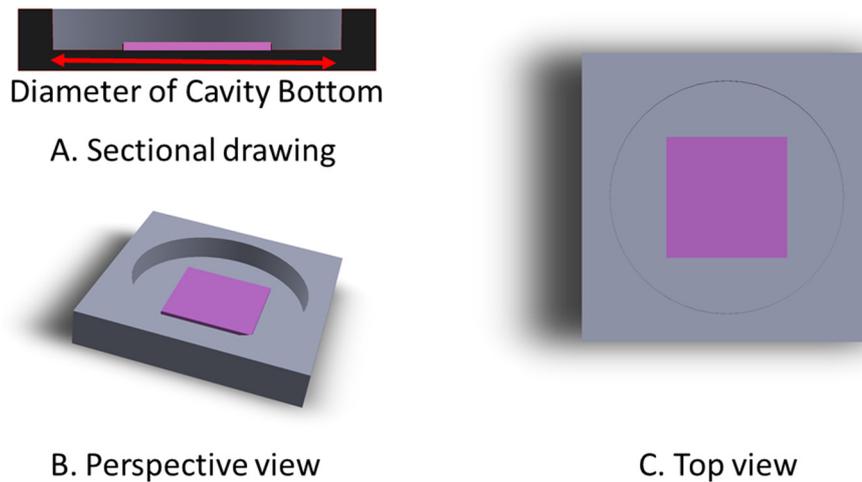


Figure 4. 3D model of a DUV-LED's cavity.

Figure 5 present the simulated LEEs of DUV-LEDs with Al, Au, or Ag metal cavity bottom reflectors of various diameters, whose reflectivity at a wavelength of 185 nm were 92%, 40%, and 25%, respectively. As evident, the cavity with the Al reflector exhibited the highest LEE at a given diameter, and with a gradual increase in the diameter of the cavity bottom, the LEE increased only slightly. These results indicate that Al reflectors outperform the other two types of reflectors.

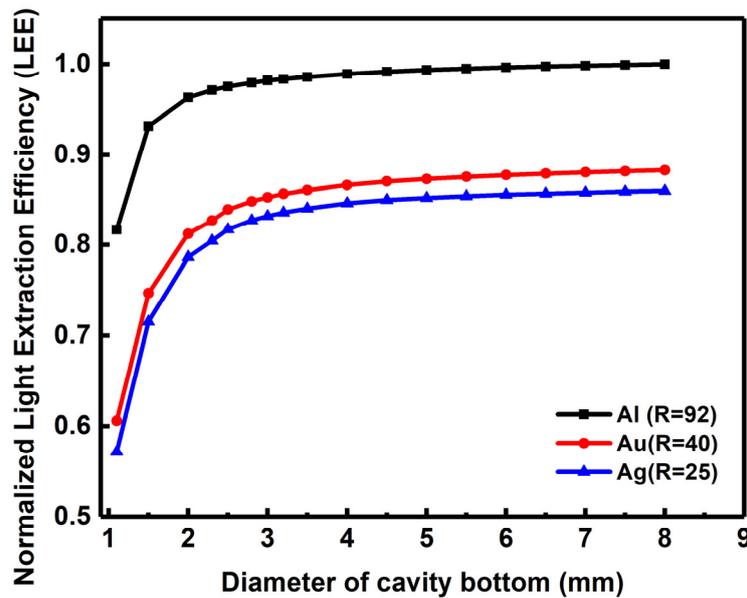


Figure 5. Simulated light-extraction efficiencies (LEEs) of DUV-LEDs with cavity-bottom diameters of 1.1–8 mm.

Figure 6 presents a comparison of the improvement in the LEE performance of the three investigated types of metal cavities. At a cavity diameter of 1.1 mm, Al (42.97%) and Au (6.06%) exhibited larger improvements than did Ag, which had the lowest reflectance; the same trends were found at diameters of 3 and 8 mm, with Al (18% and 16%, respectively) and Au (2.5% and 2.62%, respectively) exhibiting larger improvements than did Ag.

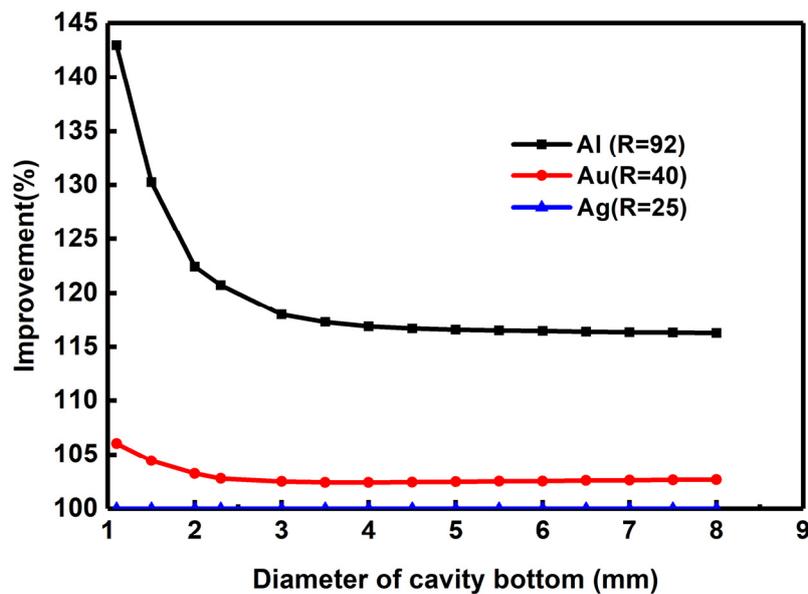


Figure 6. Simulated improvements in the LEE performance levels of DUV-LEDs with Al reflector cavities relative to those of DUV-LEDs with Ag and Au cavities.

2.3. Ray-Tracing Simulation of a DUV-LED's Tilt Angle of Reflection

To improve the LEE of the proposed design, the second optimized parameter was the tilt angle of the DUV-LED module (Figure 7). Figure 8 presents the simulated LEEs of Al-based DUV-LEDs with various tilt angles and cavity diameters; as shown, the shorter the cavity diameter, the wider is the tilt angle and higher is the LEE. Conversely, when the cavity diameter is long, the tilt angle's effect on the LEE does not change significantly. Because the DUV-LED's far-field emission is similar to that of the heart (Figure 3), the chip has a large lateral light. When the aluminum reflective cavity diameter is short or the tilt angle is narrow, the lateral light of the UVC LED is unlikely to be reflected. However, when the cavity diameter is more than 3 mm or the sidewall inclination is greater than 60° , the light output can be complete because of the relationship of the far-field emission light type. This is close to the light output efficiency of the DUV-LED chip without external optical loss. However, the cavity is designed for minimum size and maximum light output efficiency.

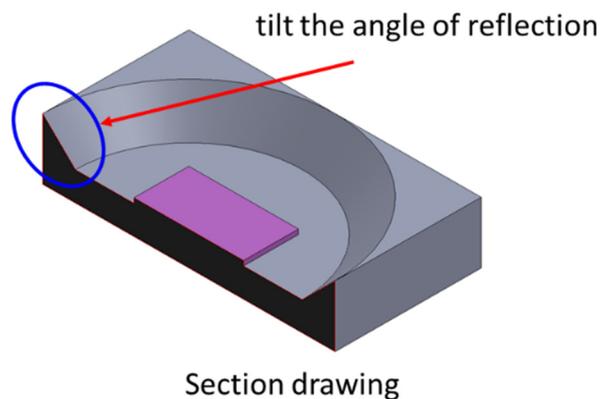


Figure 7. Three-dimensional illustration of the DUV-LED cavity showing the tilt angle of reflection.

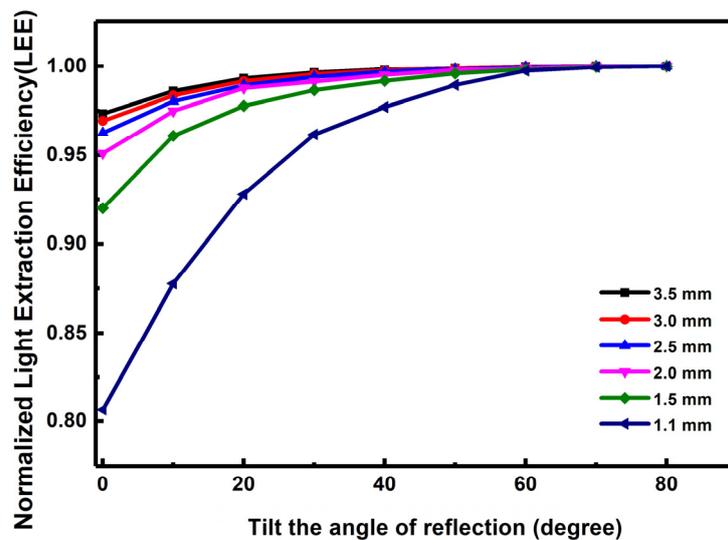


Figure 8. Simulated LEEs of DUV-LEDs with various tilt angles and cavity diameters.

3. Experiment

Fabrication

Figure 9 illustrates the packaging process of the proposed DUV-LED. In this process, (a) an aluminum plate with a sheet thickness of 1 mm and identical in size to the alumina ceramic substrate is submitted for high-precision machining, with an aperture diameter of 3 mm and inclination angle of 60 degrees. When the hole has been finished, (b) the aluminum plate is processed and the holes and sidewalls are polished (c) a ceramic substrate is prepared whose electrode material is alumina, (d) bond the peak wavelength to 285-nm DUV-LED Chip to the ceramic substrate by pressure bonding. Next, (e) the aluminum plate holes are bonded to the DUV-LED ceramic substrate and the wafer is placed on the center point of the aperture. Finally, (f) the finished DUV-LED ceramic substrate is cut, and (g) the ultraviolet LEDs are completed by optimizing the diameter and tilt of the aluminum sidewall.

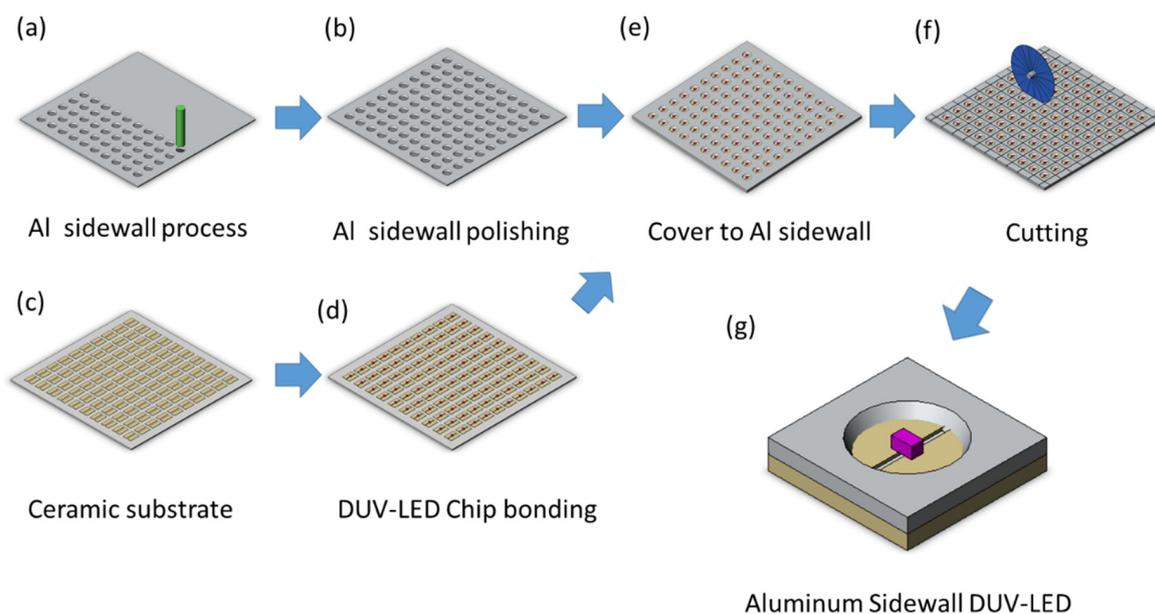


Figure 9. Procedure used to fabricate (a,b) aluminum sidewall and (c,d) bonding DUV-LED on the ceramic substrate; (e–g) the aluminum plate holes was covered to obtain the ultraviolet LEDs are completed by optimizing the diameter and tilt of the aluminum sidewall.

4. Results and Discussion

Figure 10 displays a top view of the packaged aluminum sidewall DUV-LED sample and the emission pattern of the 285-nm DUV-LED. Figure 11 presents a comparison of EQEs measured for DUV-LEDs packaged using both the conventional and proposed packaging methods at various forward currents, and Figure 12 shows the corresponding light output values measured at driving currents of 0–100 mA. As shown, in this driving current range, the proposed DUV-LED with the optimized aluminum cavity sidewall outperformed the conventionally packaged DUV-LED in terms of output power and lighting efficiency. For example, at 100 mA, the light output values of the conventional and optimized DUV-LEDs were 4.46 and 5.28 mW, respectively, representing an optimized increase of 18.38%. In addition, at 60 mA, the normalized EQE of the optimized DUV-LEDs was 14.38% higher than that of the conventional ones. The conventional package generated leakage in the low current region; no such problem was observed in the high current region. These improvements are attributable to two factors, namely the high reflectivity of the aluminum cavity and sidewall in the UV range and the use of an optimized cavity diameter and tilt angle for reflection.

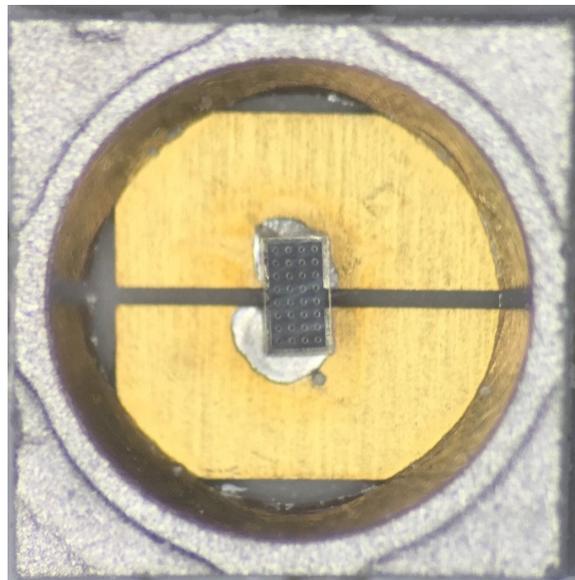


Figure 10. Top views of a packaged aluminum sidewall DUV-LED.

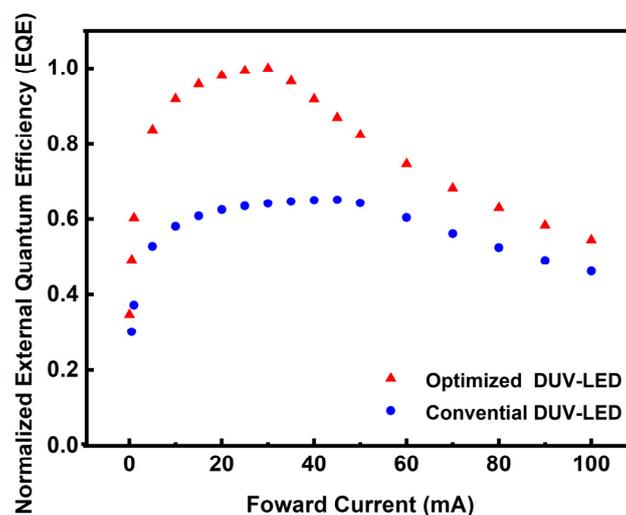


Figure 11. Measured external quantum efficiency (EQEs) of the DUV-LEDs packaged through the conventional and proposed packaging methods plotted against forward current.

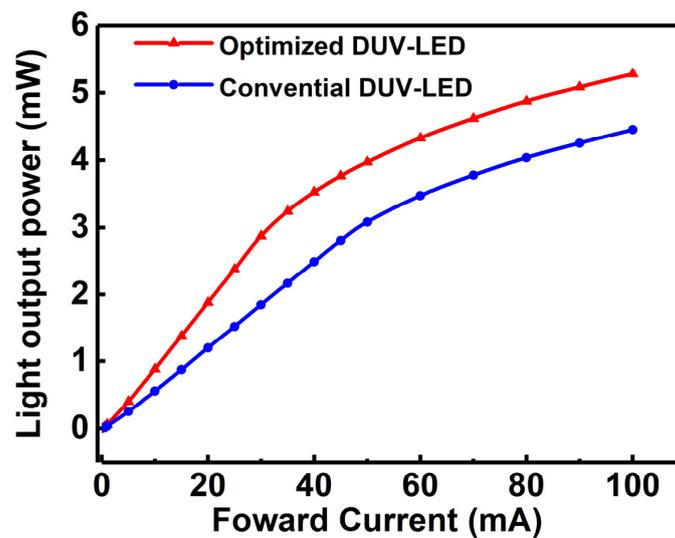


Figure 12. Measured light output values of the DUV-LEDs packaged through conventional and proposed packaging versus forward current methods plotted against forward current.

5. Conclusions

The aluminum cavity bottom diameter and sidewall tilt angle of a typical DUV-LED were optimized through ray-tracing simulations to determine the optimum parameters for improving light output power. The experiments demonstrated that the optimized 285-nm DUV-LED with a sidewall tilt angle of 60 degrees and packaging size of more than 3.0 mm had increased light output power by 18.38%. The proposed architecture is feasible as a compact solution to AlGaIn-based DUV-LED packaging with high light-extraction efficiency.

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