



Article Analytical Current-Voltage Model for Gate-All-Around Transistor with Poly-Crystalline Silicon Channel

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Abstract: Poly-crystalline silicon channel transistors have been used as a display TFT for a long time and have recently been used in a 3D vertical NAND Flash which is a transistor with 2D plane NAND upright. In addition, multi-gate transistors such as FinFETs and a gate-all-around (GAA) structure has been used to suppress the short-channel effects for logic/analog and memory applications. Compact models for poly-crystalline silicon (poly-silicon) channel planar TFTs and single crystalline silicon channel GAA MOSFETs have been developed separately, however, there are few models consider these two physics at the same time. In this work, we derived new analytical current-voltage model for GAA transistor with poly-silicon channel by considering the cylindrical coordinates and the grain boundary effect. Based on the derived formula, the compact I-V model for various operating regions and threshold voltage was proposed for the first time. The proposed model was compared with the measured data and good agreements were observed.

Keywords: poly-crystalline silicon; gate-all-around MOSFET; compact model

1. Introduction

With growing technology, compact and faster semiconductor transistors are required. To achieve this, the channel length of a device was reduced. However, as the channel length is reduced to a nanoscale region, the performance of the scaled down devices is degraded by the short-channel effects [1,2]. To avoid short-channel effects, there have been many studies on multi-gate transistors such as FinFETs and gate-all-around (GAA) MOSFETs. The common feature of these structures is that the gate surrounds the channel. In addition, in display application, poly-silicon has a faster electron field-effect mobility speed than a-Si, TFTs using poly-silicon have been used instead of a-Si TFTs [3]. Moreover, in a memory application pursuing the rapid integration density path of the floating gate beyond 20 nm, 3D NAND device has been adopted as the next generation solution. Majority of the solutions presented recently use a deposited poly-silicon channel [4]. Meanwhile, many studies have been conducted on compact modeling of GAA MOSFETs [5–7] and polysilicon channel MOSFETs [8–10], respectively. The existing GAA MOSFETs papers considered cylindrical coordinates according to the device structure. Compact modeling was performed but using single-crystalline silicon and grain boundary effect which is represent using poly-silicon was not considered at all. Similarly, the existing poly-silicon TFTs papers considered the grain boundary effect by using poly-silicon. Although compact modeling was performed, cylindrical coordinates was not considered since the device is planar TFTs structure. However, there is only one published paper considering cylindrical coordinates and grain boundary effect. In Fei Yu's studies [11], which modeled the I-V characteristics through the Lambert-W function and some approximation techniques, the surface potential of poly-silicon GAA transistors

have been studied. However, the result of the model is not intuitive to implement SPICE simulation because of the complexity of the Equation and to understand the device operation and major electrical characteristics (e.g. threshold voltage) per operating domain. This issue is most often attributed to the use of GAA's cylindrical coordinates and additional trap charge at the grain boundary in a poly-silicon channel. In this work, to solve this issue, by using proper approximations for each operating region like as D. Jimenez's work [12] we propose analytical simple drain current and threshold voltage model which can give useful and intuitive Equation for poly-Si GAA transistor.

2. Analytical Drain Current Model for GAA Transistor with Poly-Crystalline Silicon Channel

2.1. Electrostatic Potential Modeling

We considered a doped cylindrical GAA MOSFET, as shown in Figure 1. A highly doped drain and source regions ($\approx 10^{20} \text{ cm}^{-3}$) have been assumed. Following the gradual channel approximation and considering the cylindrical coordinates, we can express the Poisson's Equation as:

$$\frac{d^2\varphi}{dr^2} + \frac{1}{r}\frac{d\varphi}{dr} = \frac{q}{\varepsilon_{si}}[n(r) + N_{TA}^- + N_a]$$
(1)

where q is the electron charge, ε_{si} is the poly-silicon permittivity, n(r) is the free carrier charge density, which is expressed as n(r) = $n_i \exp(q(\varphi - V_n)/kT)$, N_{TA}^- is the ionized acceptor like trap density, which is expressed as $N_{TA}^- = g_{c1}[\pi kT/\sin(\pi kT/E_1)]\exp(-E_c/E_1)\exp(q(\varphi - V_n)/E_1)$ and N_a is the uniform doping concentration in the poly-silicon body. In this density Equation, n_i is the intrinsic carrier concentration, V_n is the channel potential, k is the Boltzmann constant, φ is the electrostatic potential, g_{c1} is the density of states at the conduction band energy level, E_1 is the inverse slope of states, and E_c is the conduction band energy level. In addition, the localized acceptor-like states, N_{TA}^- , which are important for the n-channel device operation may be divided into two groups: 'deep localized acceptor states' and 'tail states' and E_1 is the characteristic energy slope of the density of localized acceptor states [13].

Equation (1) must satisfy three boundary conditions:

$$\frac{d\varphi}{dr}(r=0) = 0 \tag{2a}$$

$$\varphi(\mathbf{r} = \mathbf{R}) = \varphi_s \tag{2b}$$

$$\varphi(\mathbf{r}=0) = \varphi_0 \tag{2c}$$

where R is the radius of the channel so φ_s is the surface potential and φ_0 is midpoint potential. To apply boundary conditions, Equation (1) should be rewritten using V_{str} and V_{sub} , which determine dominant term in Equation (1). In strong inversion region, where V_{gs} is larger than V_{str} , the density of free carrier charges become dominant. We define V_{str} as (Appendix A):

$$V_{str} = V_{fb} + V_n + \frac{\frac{E_1 \cdot kT}{q} \cdot \ln\left(\frac{n_i}{g_{c1}[\pi kT/\sin[\pi kT/E_1]]\exp(-E_c/E_1)}\right)}{+\frac{q}{4\epsilon_{si}}\left[\left[R + t_{0x}\right]^2 - R^2\right]} \\ \cdot \left[2n_i \exp\left[\frac{\frac{E_1 \cdot n_i}{q} \cdot \ln\left[\frac{n_i}{g_{c1}[\pi kT/\sin[\pi kT/E_1]]\exp[-E_c/E_1]}\right]}{\frac{kT}{q} - \frac{E_1}{q}}\right] + N_a\right]$$
(3)

where V_{fb} is the flat band voltage and t_{ox} is the oxide thickness. Hence Equation (1) can be rewritten as

$$\frac{d^2\varphi}{dr^2} + \frac{1}{r}\frac{d\varphi}{dr} = \frac{q}{\varepsilon_{si}} \left[n_i exp\left(\frac{q(\varphi - V_n)}{kT}\right) \right]$$
(4)

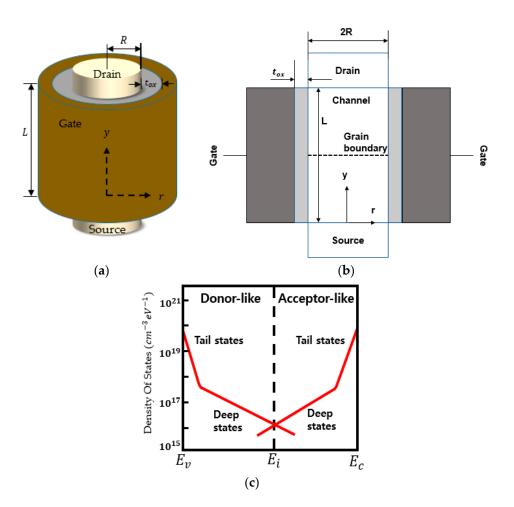


Figure 1. (**a**) 3-D scheme of the GAA MOSFETs with poly-silicon channel; (**b**) Cross-section of the GAA poly-silicon channel transistor; (**c**) Trap of exponential distribution in the poly-silicon channel.

When V_{gs} is smaller than V_{str} , we divide the subthreshold region by defining V_{sub} , which becomes dominant between the trap density and the ionized acceptor concentration.

$$V_{sub} = V_{fb} + V_n + \frac{\frac{E_1}{q} \cdot \ln\left(\frac{N_a}{g_{c1}[\pi kT/\sin[\pi kT/E_1]]\exp(-E_c/E_1)}\right)}{\frac{kT}{q} - \frac{E_1}{q}} + \frac{q}{4\varepsilon_{si}} \left[[R + t_{ox}]^2 - R^2 \right] + \frac{Q}{q} \left[\frac{E_1}{q} \cdot \ln\left[\frac{n_i}{g_{c1}[\pi kT/\sin[\pi kT/E_1]]\exp[-E_c/E_1]}\right]}{\frac{kT}{q} - \frac{E_1}{q}} \right]$$
(5)

When $V_{gs} < V_{sub}$, the body doping charge term becomes dominant in Equation (1). We used the term n_b in Equation (6) instead of N_a . As a result, Equation (1) which considered only N_a term can be expressed including electrostatic potential:

$$\frac{d^2\varphi}{dr^2} + \frac{1}{r}\frac{d\varphi}{dr} = \frac{q}{\varepsilon_{si}} \left[n_b exp\left(\frac{q(\varphi - V_n)}{V_{U_{\perp}b}}\right) \right]$$
(6)

where

$$a = \frac{\ln(2N_{a}) - \ln\{N_{a} + g_{c1}[\pi kT / \sin[\pi kT / E_{1}]] \exp\{-E_{c} / E_{1}\} exp\{-q \cdot V_{n} / E_{1}\}}{V_{n} + \frac{E_{1}}{q} \ln\left(\frac{q \cdot N_{a}}{E_{1}}\right)}$$

$$b = \ln\{N_{a} + g_{c1}\left[\frac{\pi kT}{\sin[\pi kT / E_{1}]}\right] \exp\{\frac{-E_{c}}{E_{1}}\} \exp\{\frac{-q \cdot V_{n}}{E_{1}}\}\}$$

$$n_{b} = exp(aV_{n} + b)$$

$$V_{U \ b} = \frac{1}{k}$$
(7)

When $V_{str} > V_{gs} > V_{sub}$, the trap charge term becomes dominant in Equation (1):

$$\frac{d^2\varphi}{dr^2} + \frac{1}{r}\frac{d\varphi}{dr} = \frac{q}{\varepsilon_{si}} \left[g_{c1} \left[\frac{\pi kT}{\sin[\pi kT/E_1]} \right] \exp\left[\frac{-E_c}{E_1} \right] \exp\left[\frac{q[\varphi - V_n]}{E_1} \right] \right]$$
(8)

From Equations (4), (6) and (8), we get a unified Poisson's Equation in the different operational regions as:

$$\frac{d^2\varphi}{dr^2} + \frac{1}{r}\frac{d\varphi}{dr} = \frac{q}{\varepsilon_{si}} \left[n_U exp\left(\frac{(\varphi - V_n)}{V_U}\right) \right]$$
(9)

where n_U is n_i , n_b and $g_{c1}[\pi kT / \sin(\pi kT / E_1)] \exp(-E_c / E_1)$. V_U is kT/q, V_{U_b} and E_1 respectively. To derive electrostatic potential, we use Debye length term and well-known mathematical solution $(z = A - 2\ln(Br^2 + 1))$, where A and B are constant). As a result, the electrostatic potential function can be expressed as [11,12]:

$$\varphi_U(r) = V_n - 2V_U ln \left\{ \frac{R}{2L_U \beta_U} \left[1 - \frac{\beta_U^2 r^2}{R^2} \right] \right\}$$
(10)

where $B = -\beta_U^2/R^2$. Total mobile charge can be written as $Q = C_{ox}(V_{gs} - V_{fb} - \varphi_s)$, where C_{ox} is the gate oxide capacitance, which is expressed as $C_{ox} = \varepsilon_{ox}/(Rln(1 + t_{ox}/R))$. From Gauss's law, the following relation is satisfied:

$$C_{ox} \left(V_{gs} - V_{fb} - \varphi_s \right) = Q = \varepsilon_{si} \left. \frac{d\varphi}{dr} \right|_{r=R}$$
(11)

By substituting Equation (10) into Equation (11), the function about β_U and V is derived:

$$\frac{\left(V_{gs} - V_{fb} - V\right)}{2V_{U}} - \ln\frac{2L_{U}}{R} = \ln\beta_{U} - \ln\left(1 - \beta_{U}^{2}\right) + \eta\frac{\beta_{U}^{2}}{1 - \beta_{U}^{2}}$$
(12)

where $\eta = 2\varepsilon_{si}/C_{ox}R$. We can rewrite Equation (12) as substituting $\beta_U^2/1 - \beta_U^2$ as K_U . So, Equation (12) becomes following Equation (13):

$$\frac{\left(V_{g^{s}} - V_{fb} - V\right)}{2V_{U}} - \ln\frac{2L_{U}}{R} = \ln(K_{U}) - \frac{1}{2}\ln\left(\frac{K_{U}}{1 + K_{U}}\right) + \eta K_{U}$$
(13)

By using Lambert W function [14], β_U can be solved.

$$K_{U} = \frac{W_{0}\left\{2\eta \cdot exp\left[\frac{(V_{gs} - V_{fb} - V)}{2V_{U}} - ln\frac{2L_{U}}{R}\right]\right\}}{2\eta} + w(y, y', y'') + \epsilon$$
(14)

The first term in Equation (14) is the initial solution (K_{U0}). The second and third terms in Equation (14) are the correction term to improve the accuracy [15]. The second term w(y, y', y'') is expressed as w(y, y', y'') = -(y/y'/(1-0.5yy''/y'/y')), where $y = K_{U0}^2 + K_{U0} - \exp\{2[(V_{gs} - V_{fb} - V)/2V_U - ln(2L_U/R) - \eta \cdot K_{U0}]\}$. The third term can be calculated by

$$\epsilon = \left[-b + \left(b^2 - 4ac\right)^{\frac{1}{2}} \right] / 2a, \text{ where a} = 1 - 2\eta^2 exp(2\left[\left(V_{gs} - V_{fb} - V\right) / 2V_U - ln(2L_U/R) - \eta \cdot K_{U1} \right], \\ b = 1 + 2\left[K_{U1} + \eta exp(2\left[\left(V_{gs} - V_{fb} - V\right) / 2V_U - ln(2L_U/R) - \eta \cdot K_{U1} \right], \\ exp\left\{ 2\left[\left(V_{gs} - V_{fb} - V\right) / 2V_U - ln(2L_U/R) - \eta \cdot K_{U1} \right] \right\} \text{ and } K_{U1} = K_{U0} + w(y, y', y''). \text{ As a result, } \beta_U \text{ can be solved from Equations (10) and (11) for a given } V_{gs}. We can then obtain the electrostatic potential by substituting } \beta_U \text{ into the RHS of Equation (10). To obtain the unified electrostatic potential, we use the smoothing function [9]:}$$

$$\varphi_{s_sub} = \frac{1}{m_T} \ln \left[\frac{1}{\frac{1}{exp(m_T \varphi_{s_body})} + \frac{1}{exp(m_T \varphi_{s_trap})}} \right]$$
(15)

$$\varphi_s = \frac{1}{m_T} \ln \left[\frac{1}{\frac{1}{exp[m_T \varphi_{s_free}]} + \frac{1}{exp[m_T \varphi_{s_sub}]}} \right]$$
(16)

where φ_{s_sub} is the electric surface potential result considering the two terms, i.e., body doping and trap density in the subtreshold region. φ_{s_body} and φ_{s_trap} are the surface potential dominated by the body uniform doping charge and trap charge, respectively. φ_{s_free} is the surface potential dominated by the free carrier charges. m_T is the weight parameter to connect the different asymptotical results.

2.2. Drain Current Modeling

We can obtain current Equation by using drift-diffusion current Equation $I_{ds} = \mu_{eff}(2\pi R)QdV/dy$. Integrating this Equation from the source to the drain and changing from the function of V to the function of β , we can get the different expression of drift-diffusion current Equation (17):

$$I_{ds_U} = \mu_{eff} \frac{2\pi R}{L} \int_0^{V_{ds}} Q_i(V) dV = \mu_{eff} \frac{2\pi R}{L} \int_{\beta_{U_S}}^{\beta_{U_D}} Q_i(\beta_U) \frac{dV}{d\beta_U} d\beta_U$$
(17)

where β_{U_s} and β_{U_D} are solved from Equation (13) corresponding to V = 0 and V = V_{Ds} respectively. Effective mobility can be defined to consider the grain boundary effect [16–18].

$$\mu_{eff} = \frac{\mu_0 * \left[\exp\left(\theta_1 \left(V_{ds}\right)^{\frac{1}{\theta_2}} - \theta_3 \left(V_{gs}\right)^{\theta_4}\right) \right]}{1 + \theta_5 \left(V_{gs}\right)^{\theta_6} + \theta_7 \left(V_{gs}\right)^{\theta_8} + \theta_9 \left(V_{ds}\right)^{\theta_{10}}}$$
(18)

where μ_0 is the low-field mobility and $\theta_1 \sim \theta_{10}$ are the fitting parameters. We can obtain $Q_i(\beta_U) = \varepsilon_{si} d\varphi_{s_U}/dr - Q_t - Q_b = \varepsilon_{si} 4V_U\beta_U^2/R(1-\beta_U^2) - Q_t - Q_b$ from Equation (10) where Q_t is trapped charge density, Q_b is bulk charge density and $dV/d\beta = -2V_U[1/\beta_U + 2\beta_U/1 - \beta_U^2 + 2\eta\beta_U/(1-\beta_U^2)^2]$ from Equation (12). By substituting this terms to Equation (17), we can get the drain current Equation (19):

$$I_{ds_U} = \mu_{eff} \varepsilon_{si} \frac{16\pi}{L} V_U^2 \cdot \left[\frac{1}{2} \ln \left[1 - \beta_U^2 \right] + \frac{1 - \eta}{1 - \beta_U^2} + \frac{\eta}{2[1 - \beta_U^2]^2} \right]_{\beta_{U_D}}^{\beta_{U_D}}$$

$$-2\mu_{eff} \frac{2\pi R}{L} V_U (Q_t + Q_b) \left[\ln \frac{\beta_U}{1 - \beta_U^2} \right] + \frac{\eta}{1 - \beta_U} \left]_{\beta_{U_D}}^{\beta_{U_D}}$$
(19)

Due to the difference in the current derivation method and approximation, there is a difference between the second term of Equation (19) and the current Equation in [11]. To calculate Q_t , we should

calculate $\iint -qg_{c1}[\pi kT / \sin(\pi kT / E_1)] \exp(-E_c / E_1) \exp(q(\varphi - V_n) / E_1) r dr d\theta / (2\pi R)$. Instead of using this integral method, we use the trapezoid rule as:

$$Q_{t} = -qg_{c1} \left[\frac{\pi kT}{\sin[\pi kT/E_{1}]} \right] \exp\left(\frac{-E_{c}}{E_{1}}\right) \frac{\sum_{i=1}^{n-1} \left\{ \left[\exp\left(\frac{q(\varphi_{i}-V_{n})}{E_{1}}\right) + \exp\left(\frac{q(\varphi_{i+1}-V_{n})}{E_{1}}\right) \frac{h}{2} \right\}}{2} R$$
(20)

To compute the drain current at the different operation region, we define three functions of $f(\beta)$, $g(\beta)$ and $h(\beta)$:

$$f(\beta) = ln\beta_U - \ln(1 - \beta_U^2) + \eta \frac{\beta_U^2}{1 - \beta_U^2}$$
(21a)

$$g(\beta) = \left[\frac{1}{2}\ln(1-\beta_{U}^{2}) + \frac{1-\eta}{1-\beta_{U}^{2}} + \frac{\eta}{2(1-\beta_{U}^{2})^{2}}\right]_{\beta_{U_{D}}}^{\beta_{U_{D}}}$$
(21b)

$$\mathbf{h}(\beta) = \left[\ln \frac{\beta_U}{1 - \beta_U^2}\right] + \frac{\eta}{1 - \beta_U} \left[\frac{\beta_{U_S}}{\beta_{U_D}} \right]$$
(21c)

For a given V_{gs} and V_{ds} , β_{U_s} and β_{U_D} can be calculated from Equations (12) and (21a) $f(\beta_{U_s}) = (V_{gs} - V_{fb})/2V_U - \ln(2L_U/R)$ and $f(\beta_{U_D}) = (V_{gs} - V_{fb} - V_{ds})/2V_U - \ln(2L_U/R)$. Following the approach of D. Jimenez [12], we can derive the analytical drain current Equation with closed-form by carefully checking the fluctuation of $f(\beta)$ and β , which has different values at each operation region.

(1) Linear region above threshold

In this region, $f(\beta_{U_S})$, $f(\beta_{U_D}) \gg 1$, thus β_{U_S} , $\beta_{U_D} \sim 0$. Hence, the term $\eta/2(1 - \beta_{U_S,D}^2)^2$ in $g(\beta_{U_S,D})$ becomes dominant and the term $\eta/1 - \beta_{U_S,D}$ in $h(\beta_{U_S,D})$ becomes dominant. As a result, the drain current Equation is derived as:

$$I_{ds_U} = \mu_{eff} \frac{W}{L} C_{ox} \Big[\Big(V_{gs} - V_t - \frac{1}{2} V_{ds} \Big) V_{ds} \Big] - \mu_{eff} \frac{W}{L} (Q_t + Q_b) V_{ds}$$
(22)

where $V_t = V_0 - 2\eta V_U$ and $V_0 = V_{fb} + 2V_U \cdot ln(2L_U/R)$.

(2) Saturation region

In this region, $f(\beta_{U_S}) \gg 1$ and $f(\beta_{U_D}) \ll -1$ thus $\beta_{U_S} \sim 0$ and $\beta_{U_D} \sim 1$. Hence, the term $\eta/2(1-\beta_{U_S}^2)^2$ in $g(\beta_{U_S})$ becomes dominant, the term $(1-\eta)/(1-\beta_{U_D}^2)+\eta/2(1-\beta_{U_D}^2)^2$ in $g(\beta_{U_D})$ becomes dominant, the term $\eta/1-\beta_{U_S}$ in $h(\beta_{U_S})$ becomes dominant, and the term $\ln(\beta_{U_D}/(1-\beta_{U_D}^2))+\eta/(1-\beta_{U_D})$ in $h(\beta_{U_D})$ becomes dominant. As a result, the drain current Equation is derived as:

$$I_{ds_U} = \mu_{eff} \frac{W}{L} \frac{C_{ox}}{2} \left[\left(V_{gs} - V_t \right)^2 - V_U^2 \left\{ \frac{4\eta^2}{\left(1 - \exp\left(\frac{V_{gs} - V_0 - V_{ds}}{V_U}\right) \right)^2} + \frac{8\eta(1 - \eta)}{1 - \exp\left(\frac{V_{gs} - V_0 - V_{ds}}{V_U}\right)} \right\} \right] - \mu_{eff} \frac{W}{L} (Q_t + Q_b) \left[(2\eta V_U + V_{ds}) - 2V_U \left\{ \frac{\eta}{1 - \exp\left(\frac{V_{gs} - V_0 - V_{ds}}{V_U}\right)} - \ln\left(1 - \exp\left(\frac{V_{gs} - V_0 - V_{ds}}{V_U}\right) \right) \right\} \right]$$
(23)

(3) Subthreshold region

In this region, $f(\beta_{U_S})$, $f(\beta_{U_D}) \ll -1$, thus $\beta_{U_S}, \beta_{U_D} \sim 1$. Hence, the term $(1 - \eta)/(1 - \beta_{U_S,D}^2) + \eta/2(1 - \beta_{U_S,D}^2)^2$ in $g(\beta_{U_S,D})$ becomes dominant and the term

 $\ln(\beta_{U_{S,D}}/(1-\beta_{U_{S,D}}^2)) + \eta/(1-\beta_{U_{S,D}})$ in $h(\beta_{U_{S,D}})$ becomes dominant. As a result, the drain current Equation is derived as:

$$I_{ds_U} = 2\mu_{eff} \frac{W}{L} V_{U} \left\{ \frac{2\varepsilon_{si}R}{Lu^{2}} \exp\left(\frac{V_{gs}-V_{fb}}{V_{U}}\right) \left[1 - \exp\left[\frac{-V_{ds}}{V_{U}}\right] \right] -2\mu_{eff} \frac{W}{L} V_{U} \left(Q_{t} + Q_{b}\right) \left[\frac{V_{ds}}{2V_{U}} + \frac{\eta R^{2}}{4Lu^{2}} \exp\left[\frac{V_{gs}-V_{fb}}{V_{U}}\right] \left\{ 1 - \exp\left\{\frac{-V_{ds}}{V_{U}}\right\} \right\} \right]$$
(24)

Finally, to get the unified electrostatic potential, we use smoothing function:

$$I_{ds_sub} = \left[\frac{1}{\left[\frac{1}{\left[I_{ds_body}\right]^{m_v} + \frac{1}{\left[I_{ds_trap}\right]^{m_v}}}\right]^{\frac{1}{m_v}}}$$
(25)

$$I_{ds} = \left[\frac{1}{\frac{1}{[I_{ds_free}]^{m_v}} + \frac{1}{[I_{ds_sub}]^{m_v}}}\right]^{\frac{1}{m_v}}$$
(26)

where I_{ds_free} , I_{ds_body} and I_{ds_trap} are the drain current dominated by free carriers charge, body doping charge and trap charge respectively. m_v is weight parameter. As shown in the linear region, threshold voltage can be expressed as $V_t = V_0 - 2\eta V_U$. If V_t is written as a function of R, the following Equation (27) is obtained:

$$V_t = V_{fb} + 2V_U \cdot ln(2L_U) - 2V_U ln \left[R \left[1 + \frac{t_{ox}}{R} \right]^{\frac{2\varepsilon_{si}}{\varepsilon_{ox}}} \right]$$
(27)

3. Results and Discussion

Figure 2a represents the electrostatic potential from boundary conditions (Equations (2b) and (2c)). From Equation (10), the surface potential is solved by substituting r to R. Likewise, the midpoint potential is solved by substituting r to zero. Figure 2 shows that in the subthreshold region, where the gate voltage is smaller than V_{str} , the surface potential and midpoint potential are linear as the gate voltage increases. After the subthreshold region, each potential value is saturated in the strong inversion region. Figure 2b shows the surface potential at different channel potentials. The surface potential increases as V_n increases.

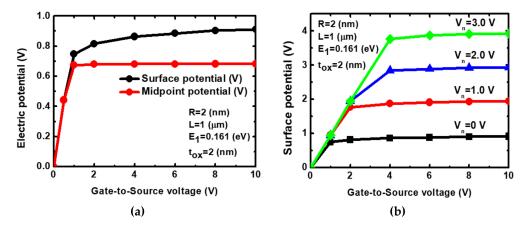


Figure 2. (a) Electrostatic surface potential and midpoint potential versus gate-to-source voltage at $V_n = 0$; (b) Electrostatic surface potential at different value of V_n

The values of the parameters in Table 1 refer to the values extracted for the fabricated device through the actual process [11,19]. We divided the current Equation into three operation regions,

as represented by Equations (22)–(24). Figure 3 shows the drain current versus drain-to-source voltage curve. This I-V curve can be drawn by using the current value of the linear region and the current value of the saturation region. As shown in Figure 3, we obtained the drain current value for different gate-to-source voltages. As the gate-to-source voltage increases, the saturation point increases and its current value increases. In addition, in this graph, we compared the GAA MOSFET devices with poly-silicon channel and the GAA MOSFET devices with single-crystalline silicon channel.

As shown in the Figure 3, devices using single-crystalline silicon channels show higher current values when compared to poly-silicon with grain boundary as expected. However, as the gate voltage increases, electrons are occupied at the trap. So the difference is smaller than when the gate voltage is low.

Figure 4 shows the graph where drain current is plotted against the gate-to-source voltage for low and high drain-to-source voltages. This I-V curve can be plotted by using the current value of the subthreshold voltage region and the current value of the linear region at low drain-to-source voltages. At high drain-to-source voltages, we used the current values of the subthreshold voltage region and the saturation region. As shown in Figure 4, the high drain-to-source voltage has a larger current value than the low drain-to-source voltage because of a lower barrier region.

Symbol (units)	Value	Symbol (units)	Value
g_{c1} (cm ⁻³ eV ⁻¹)	$2*10^{18}$	θ_3	0.002
$g_{c1}\left(\mathrm{cm}^{-3}\mathrm{eV}^{-1} ight) \ N_{a}\left(\mathrm{cm}^{-3} ight)$	$1*10^{14}$	$ heta_4$	2
$E_1(eV)$	0.1	θ_5	0.01
$L(\mu m)$	2	$ heta_6$	1
t_{ox} (nm)	27	θ_7	0.001
R (nm)	35	θ_8	3
V_{fb} (V)	0	θ_9	0.01
$\mu_0 \left(\text{cm}^2 \text{V}^{-1} \text{s}^{-1} \right)$	50	$ heta_{10}$	1
θ_1	0.5	m_T	20
θ_2	3	m_v	10

Table 1. Parameter Symbols and Values.

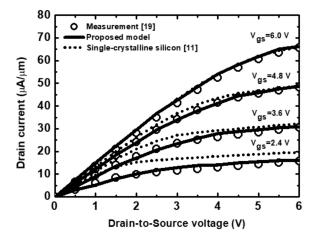


Figure 3. Comparison of drain current versus drain-to-source voltage between proposed model, experimental results and single-crystalline silicon channel GAA MOSFETs.

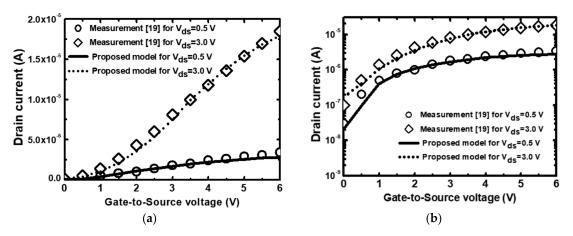


Figure 4. Comparison of drain current versus gate-to-source voltage between the proposed model and experimental results at low $I_{ds \ U}$ and high $V_{ds}(3.0 \ V)$ voltages. (a) Linear scale; (b) Log scale.

4. Conclusions

In this work, we proposed an analytical current-voltage model of GAA transistor using poly-silicon channel. The proposed model considers both GAA cylindrical coordinates and poly-silicon grain boundary trap effects. It is a channel potential-based model that calculates the potential at the center of the substrate and the surface. Using the obtained channel potentials and the D. Jimenez approach, a useful and intuitive drain current-voltage and threshold voltage model Equation are derived by taking the appropriate approximation in each transistor's operating region (linear, saturation and sub-threshold regions). The proposed model shows high consistency when compared with the measured results which is the actual performance measured by applying NH₃ plasma treatment to reduce the trap-state density on poly-silicon TFT with GAA structure.

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Conflicts of Interest: The authors declare no conflict of interest.

Appendix A

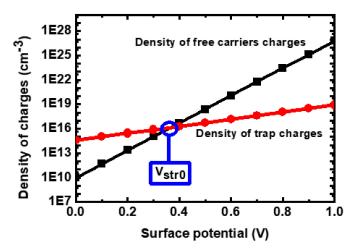


Figure A1. The density of charges versus surface potential.

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As shown in Figure A1, at specific value, density of free carrier charges over density of trap charges. We defined this crossover point as V_{str0} and we expressed it as function of voltage shown in the following method.

$$n_i \exp\left(\frac{q(V_{str0} - V_n)}{kT}\right) = g_{c1}\left[\frac{\pi kT}{\sin(\pi kT/E_1)}\right] \exp\left(\frac{-E_c}{E_1}\right) \exp\left(\frac{q(V_{str0} - V_n)}{E_1}\right)$$
(A1)

 V_{str0} can be defined by Equation (A1).

$$V_{str0} = V_n + \frac{\frac{E_1}{q} \cdot \frac{kT}{q} \cdot \ln\left(\frac{n_i}{g_{c1}[\pi kT/\sin(\pi kT/E_1)]\exp(-E_c/E_1)}\right)}{\frac{kT}{q} - \frac{E_1}{q}}$$
(A2)

At this point, density of free carrier charges and density of trap charges are same value. So rewrite the Poisson's Equation (1).

$$\frac{d^2\varphi}{dr^2} + \frac{1}{r}\frac{d\varphi}{dr} = \frac{q}{\varepsilon_{si}} \left[2n_i \exp\left(\frac{q(V_{str0} - V_n)}{kT}\right) + N_a \right]$$
(A3)

Integrating the Equation (A3) results:

$$\frac{d\varphi}{dr} = \frac{qr}{2\varepsilon_{si}} \left[2n_i \exp\left(\frac{\frac{E_1}{q} \cdot \ln\left(\frac{n_i}{g_{c1}[\pi kT/\sin(\pi kT/E_1)]\exp(-E_c/E_1)}\right)}{\frac{kT}{q} - \frac{E_1}{q}}\right) + N_a \right]$$
(A4)

From voltage relationship Equation, V_{str} can be written as $V_{str} = V_{str0} + V_{fb} + V_{ox}$ and V_{ox} can be calculated by:

$$V_{ox} = \int_{R+t_{ox}}^{K} -\frac{d\varphi}{dr} dr$$

$$= \frac{q}{4\varepsilon_{si}} \left[2n_i \exp\left(\frac{\frac{E_1}{q} \cdot \ln\left(\frac{\pi kT}{g_{c1}[\pi kT/\sin(\pi kT/E_1)]\exp(-E_c/E_1)}\right)}{\frac{kT}{q} - \frac{E_1}{q}}\right) + N_a \right]$$

$$\cdot \left[(R+t_{ox})^2 - R^2 \right]$$
(A5)

Hence, substitute Equations (A2) and (A5) into voltage relationship Equation (A6):

$$V_{str} = V_{fb} + V_n + \frac{\frac{E_1 \cdot kT}{q} \cdot \ln\left(\frac{n_i}{g_{c1}[\pi kT / \sin[\pi kT / E_1]] \exp(-E_c / E_1)}\right)}{\frac{kT}{q} - \frac{E_1}{q}} + \frac{q}{4\varepsilon_{si}} \left[[R + t_{ox}]^2 - R^2 \right]$$

$$\left[2n_i \exp\left[\frac{\frac{E_1}{q} \cdot \ln\left[\frac{\pi}{g_{c1}[\pi kT / \sin[\pi kT / E_1]] \exp[-E_c / E_1]}\right]}{\frac{kT}{q} - \frac{E_1}{q}} \right] + N_a \right]$$
(A6)

References

- El Hamid, H.A.; Iñíguez, B.; Guitart, J.R. Analytical model of the threshold voltage and subthreshold swing of undoped cylindrical gate-all-around-based MOSFETs. *IEEE Trans. Electron Devices* 2007, 54, 572–579. [CrossRef]
- 2. Chen, Y.; Luo, J. A comparative study of double-gate and surrounding gate MOSFETs in strong inversion and accumulation using an analytical model. *Integration* **2001**, *1*, *6*.

- Fortunato, G. Polycrystalline silicon thin-film transistors: A continuous evolving technology. *Thin Solid Film*. 1997, 296, 82–90. [CrossRef]
- Nowak, E.; Kim, J.H.; Kwon, H.; Sim, J.S.; Lim, S.H.; Lee, K.H.; Park, Y.K.; Choi, J.H.; Chung, C. Intrinsic fluctuations in vertical NAND flash memories. In Proceedings of the 2012 Symposium on VLSI Technology (VLSIT), Honolulu, HI, USA, 12–14 June 2012; pp. 21–22.
- 5. Yu, B.; Lu, H.; Liu, M.; Taur, Y. Explicit continuous models for double-gate and surrounding-gate MOSFETs. *IEEE Trans. Electron Devices* **2007**, *54*, 2715–2722.
- 6. Yu, B.; Lu, W.Y.; Lu, H.; Taur, Y. Analytic charge model for surrounding-gate MOSFETs. *IEEE Trans. Electron Devices* **2007**, *54*, 492–496. [CrossRef]
- 7. Lu, H.; Yu, B.; Taur, Y. A unified charge model for symmetric double-gate and surrounding-gate MOSFETs. *Solid State Electron.* **2008**, *52*, 67–72. [CrossRef]
- 8. Li, X.; Deng, W.; Huang, J. A physical surface-potential-based drain current model for polysilicon thin-film transistors. *J. Semicond.* **2012**, *33*, 034005. [CrossRef]
- Deng, W.; Zheng, X.; Chen, R.; Liu, Y. Subthreshold characteristics of polysilicon TFTs. *Solid State Electron*. 2008, 52, 695–703. [CrossRef]
- 10. Chen, S.S.; Shone, F.C.; Kuo, J.B. A closed-form inversion-type polysilicon thin-film transistor dc/ac model considering the kink effect. *J. Appl. Phys.* **1995**, 77, 1776–1784. [CrossRef]
- 11. Yu, F.; Deng, W.; Huang, J.; Ma, X.; Chen, S. An Explicit Physics-Based I-V Model for Surrounding-Gate Polysilicon Transistors. *IEEE Trans. Electron Devices* **2016**, *63*, 1059–1065. [CrossRef]
- 12. Jiménez, D.; Iniguez, B.; Suñe, J.; Marsal, L.F.; Pallares, J.; Roig, J.; Flores, D. Continuous analytic IV model for surrounding-gate MOSFETs. *IEEE Electron Device Lett.* **2004**, *25*, 571–573. [CrossRef]
- Shur, M.; Hack, M. Physics of amorphous silicon based alloy field-effect transistors. J. Appl. Phys. 1984, 55, 3831–3842. [CrossRef]
- 14. Corless, R.M.; Gonnet, G.H.; Hare, D.E.; Jeffrey, D.J.; Knuth, D.E. On the Lambert W function. *Adv. Comput. Math.* **1996**, *5*, 329–359. [CrossRef]
- 15. Deng, W.; Huang, J. A physics-based approximation for the polysilicon thin-film transistor surface potential. *IEEE Electron Device Lett.* **2011**, *32*, 647–649. [CrossRef]
- Wang, M.; Wong, M.; Shi, X.; Zhang, D. Effective channel mobility of poly-silicon thin film transistors. In Proceedings of the 2006 8th International Conference on Solid-State and Integrated Circuit Technology Proceedings, Shanghai, China, 23–26 October 2006; pp. 1395–1397.
- 17. Chern, H.N.; Lee, C.L.; Lei, T.F. An analytical model for the above-threshold characteristics of polysilicon thin-film transistors. *IEEE Trans. Electron Devices* **1995**, *42*, 1240–1246. [CrossRef]
- 18. Xi, X. BSIM 4.5. 0 MOSFET Model User Manual. 2004; University of California: Berkeley, CA, USA, 2004.
- 19. Sheu, J.T.; Huang, P.C.; Sheu, T.S.; Chen, C.C.; Chen, L.A. Characteristics of gate-all-around twin poly-Si nanowire thin-film transistors. *IEEE Electron Device Lett.* **2008**, *30*, 139–141. [CrossRef]



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