

Supplementary File

Back-channel etched In-Ga-Zn-O Thin-film Transistor utilizing Selective Wet-Etching of Copper Source and Drain

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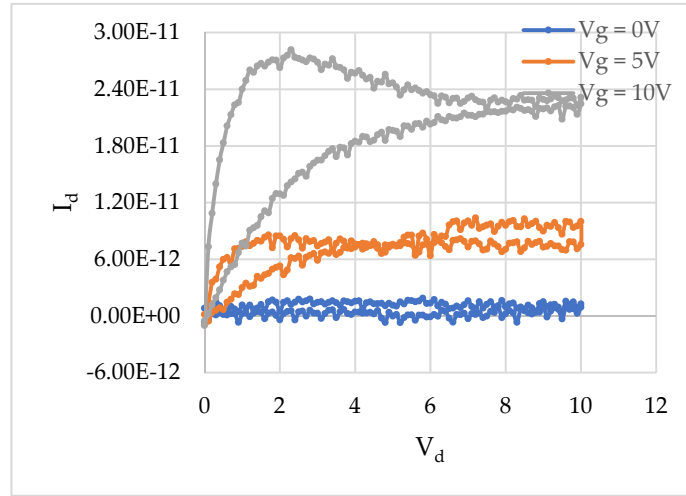
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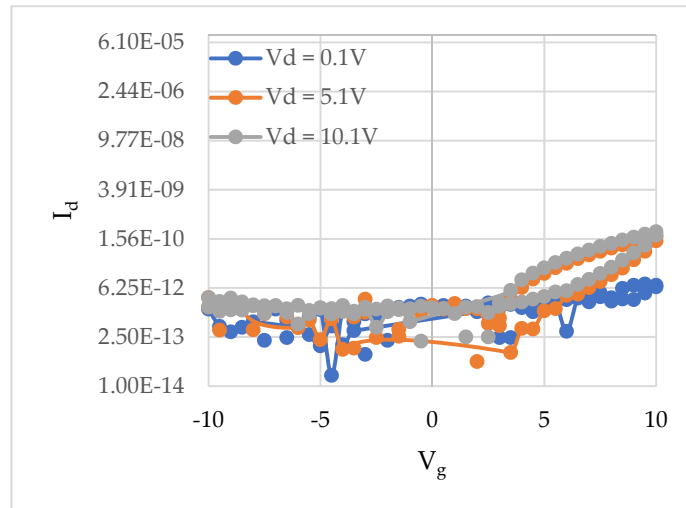
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(a)



(b)

Figure S1: (a) Output and (b) transfer characteristics of IGZO TFT without passivation layer ($W/L = 10 \mu\text{m}/20 \mu\text{m}$) after annealing in N_2 atmosphere for 1 hour.

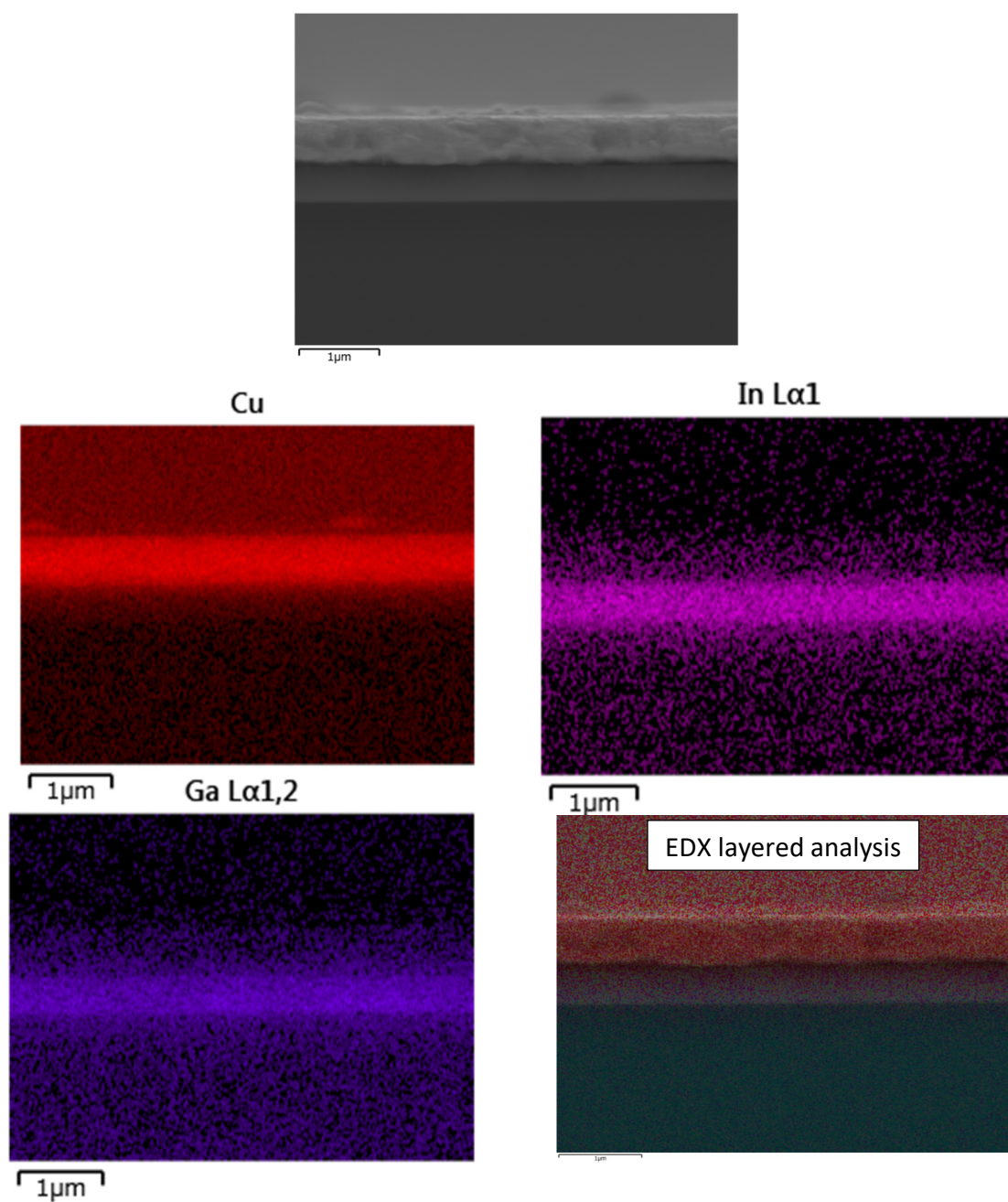


Figure S2: Mapping analysis of sample A before post annealing.

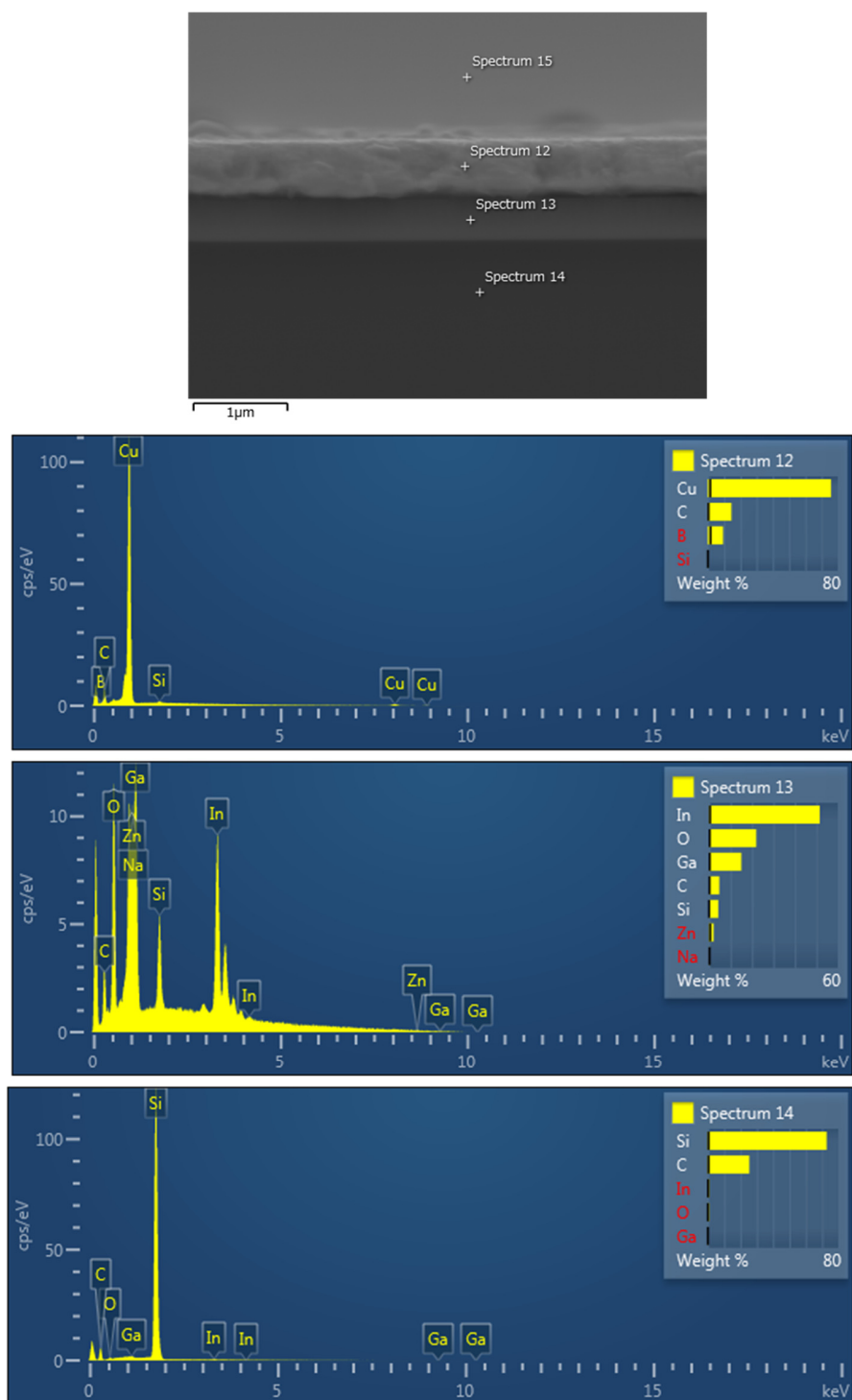


Figure S3: Spectrum analysis of sample A before post annealing.

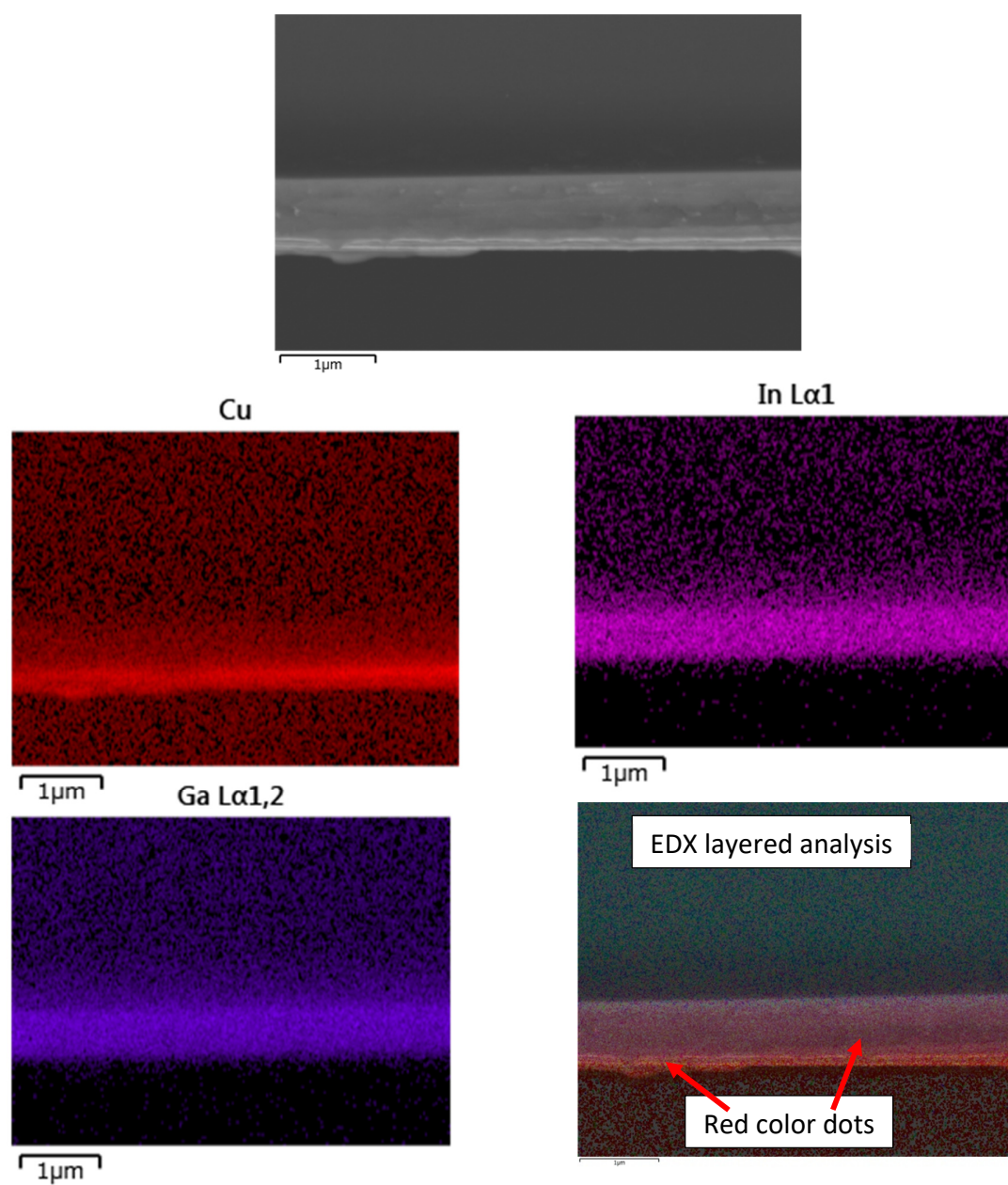


Figure S4: Mapping analysis of sample A after post annealing.

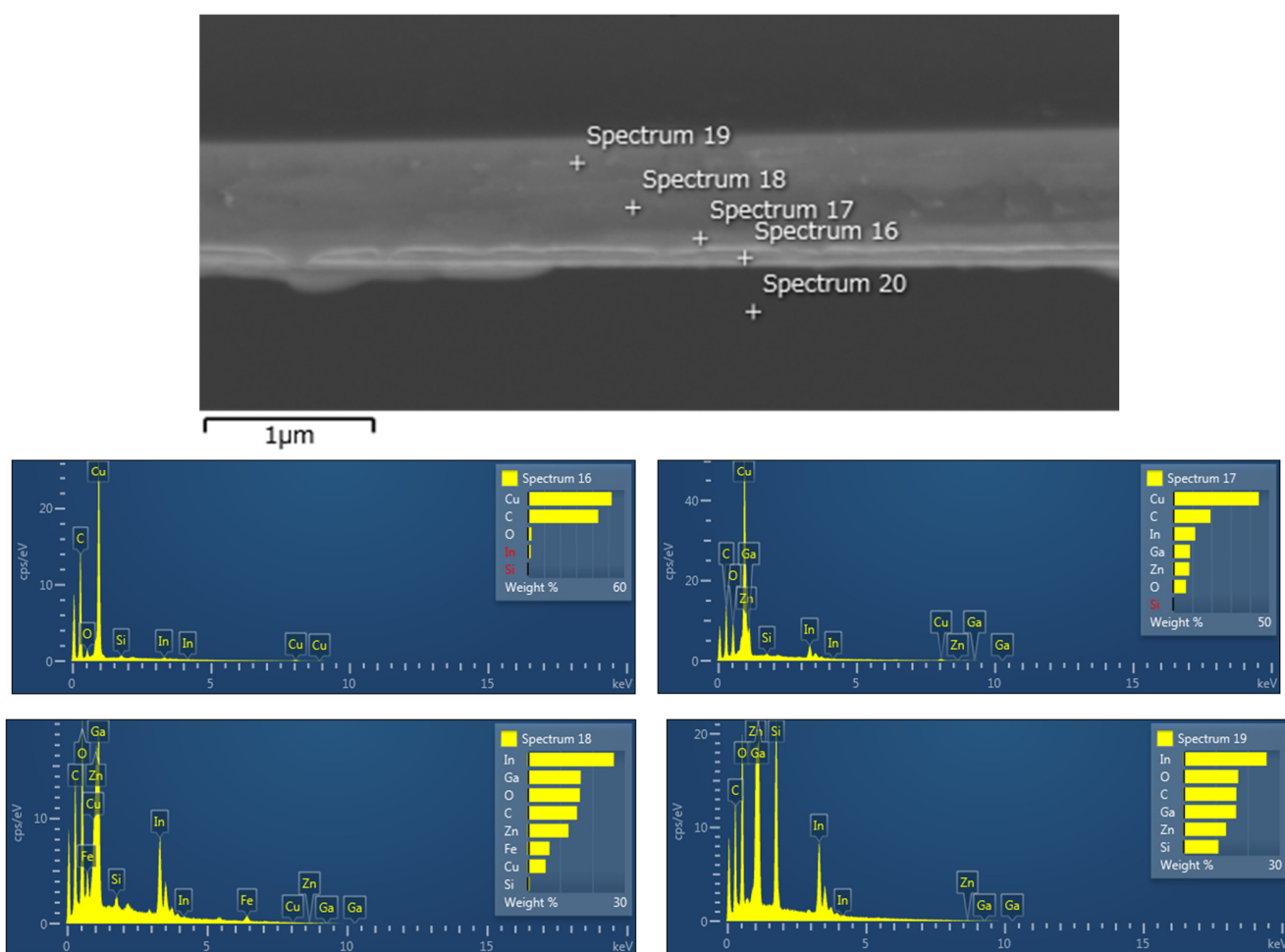


Figure S5: Spectrum analysis of sample A after post annealing.

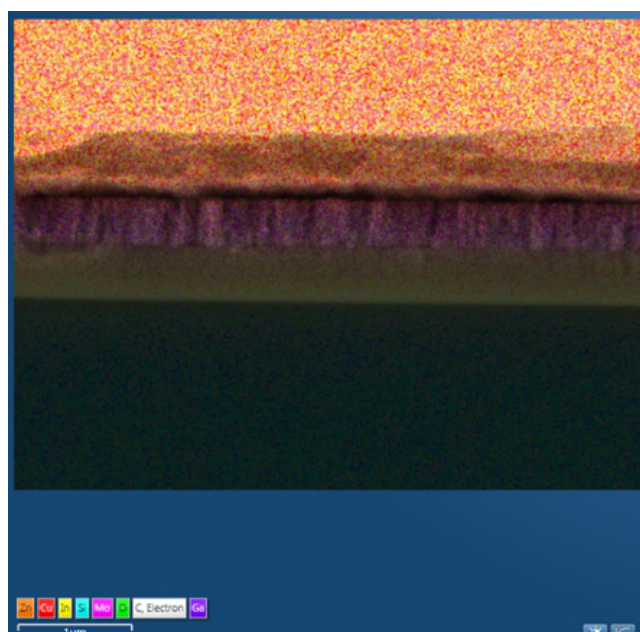


Figure S6: The EDX layered analysis of Sample B before post annealing.

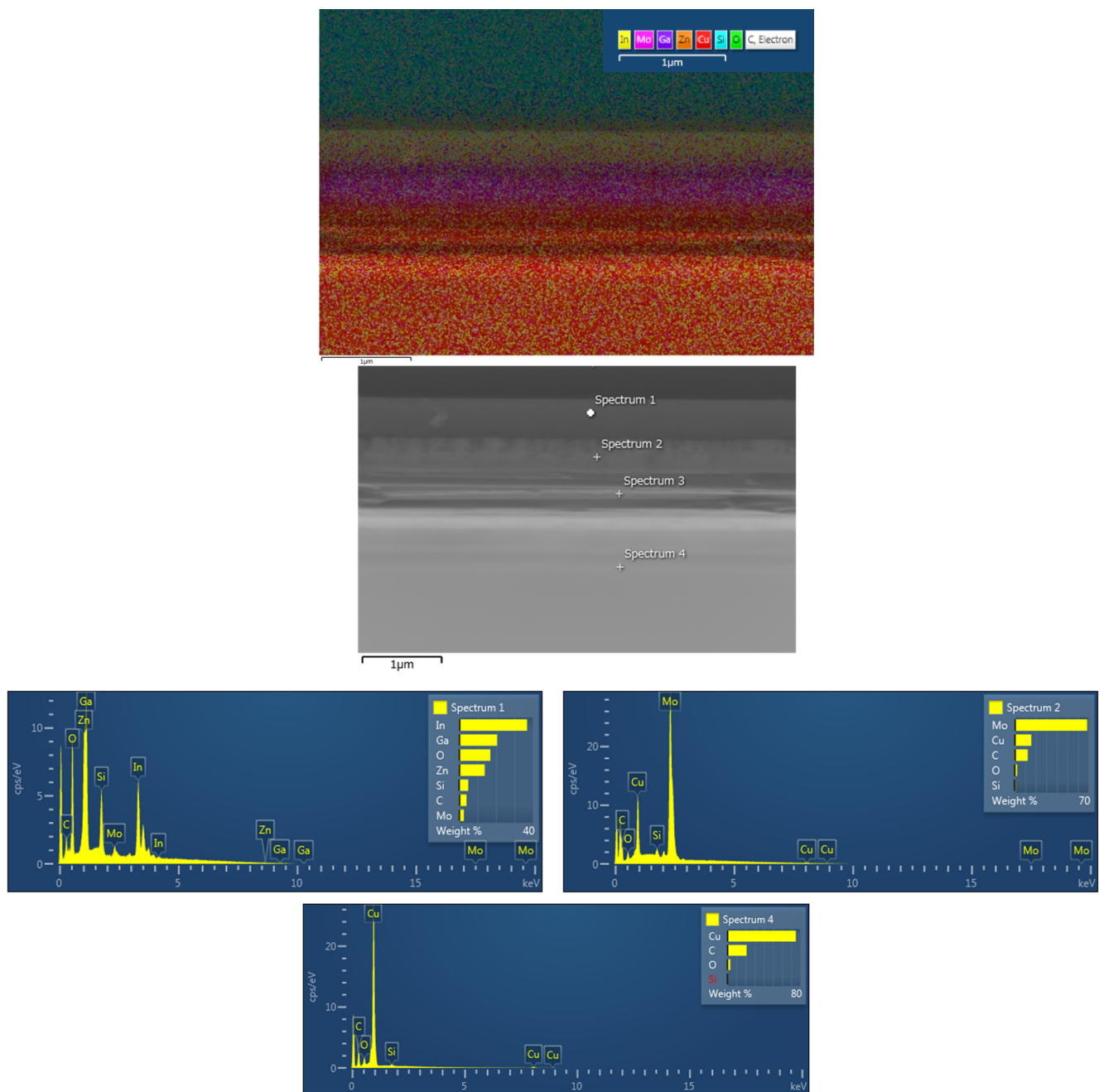


Figure S7: The EDX layered analysis and spectrum analysis of Sample B after post annealing.