Feature Papers in Hardware Security

Guest Editor:

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Deadline for manuscript submissions:
closed (30 April 2021)

Message from the Guest Editor

Dear Colleagues,

This is a Special Issue for high-quality papers by the editorial board members or those invited by the editorial office and the Editor-in-Chief.

Papers would be published, **free of charge**, in open access form after peer review.

We invite authors to submit research papers on topics related to hardware-based authentication, encryption and secure boot protocols for resource-constrained embedded systems, on novel side-channel analysis attacks and countermeasures, on PUFs for ICs and printed circuit boards (PCBs) capable of providing security, trust and detection of tamper, on hardware Trojan attacks, analysis, detection methods and countermeasures, on supply-chain authentication and hardware assurance methods, on hardware-based security and trust primitives for RFID (Radio-frequency identification), IoT, autonomous vehicle, embedded medical, and industrial control, communication and other types of critical infrastructures, and on reverse engineering techniques and countermeasures to protect ICs and IPs through obfuscation and active metering schemes.

Special Issue