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Recent Advances in Silicon-Based RFIC Design

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Deadline for manuscript submissions:

closed (1 November 2022)

Message from the Guest Editor

Aim of this Special Issue is to collect recent advances in the design of silicon-based RFIC's and related systems covering, but not limited to, the following hot application fields:

- Digital intensive RF transceiver architectures
- High speed ADCs for direct sampling RF receiver architectures
- Millimeter-wave SiGe BiCMOS front-ends and frequency synthesis
- Cryogenic CMOS RFICs for quantum engineering
- Silicon microwave-photonics integrated circuits

Welcome to contribute!











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Message from the Editor-in-Chief

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