



Dear Colleagues,

As Editor-in-Chief of the *Journal of Low Power Electronics and Applications*, I am pleased to announce the winner of the 2017 *JLPEA* Travel Award on behalf of the Evaluation Committee:

**Hao Cai**, Postdoc Fellow at the Télécom-ParisTech, whose research focuses on ultra-low power FDSOI. He is planning to present his work “Novel Pulsed-Latch Replacement in Non-Volatile Flip-Flop Core” at the IEEE Computer Society Annual Symposium on VLSI (ISVLSI 2017). The award consists of 800 Swiss Francs towards attending this conference.

Please find additional details here: <http://www.mdpi.com/journal/jlpea/awards>

It was a difficult decision with such high quality applications for the award and we thank all applicants. We wish to congratulate Dr. Hao Cai for his accomplishments.

Alexander Fish,  
Editor-in-Chief

# 2017 TRAVEL AWARD WINNER

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