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Microprocessors

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Message from the Guest Editors

Dear Colleagues,

In recent years, applying big-data technologies to field applications has resulted in several new needs: First, the processing of data across a compute continuum spanning from cloud to edge to devices, with varying capacity, architecture, etc. Second, some computations need to be made predictable, thus supporting both data-in-motion processing and larger-scale data-at-rest processing. The computation capabilities of smaller devices, such as wearables and extremely low-power-consumption sensors, need to be exploited to achieve a pervasive sensor network in a responsive and energy-efficient smart city.

This Special Issue will cover the latest advances in lowpower architectures, programming models for smart city infrastructures, data analytics methods for power or time constraints, and more. We encourage submissions showing how this next generation of microprocessors can be effectively used in field applications, by making the best of hardware features such as GPGPU acceleration, reconfigurable logics, and deep learning compute engines in real applications. Methodologies for system design and software development for such platforms and domains are also welcome.







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Editor-in-Chief

Message from the Editor-in-Chief

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